Comparative analysis of Microstrip Patch Antenna using different substrates and observe effect of changing parameter at 5.4 GHz

Brajlata Chauhan¹,  
Uttarakhand Technical University, Dehradun UK India  
Email : 1. brajlata@rediiffmail.com

Sandip Vijay² & S C Gupta³  
Department of Electronics & communication Engineering  
Dehradun Institute of Technology, Dehradun, UK India  
Email : 2. vijaysandip@gmail.com, 3. sureshprem1938@gmail.com

Abstract

In this paper, an attempt has been made to design Edge Feed Microstrip antenna structure for WLAN application using different substrate (ε_r = 2.22, 4.84,9.6, 10.5) and analyze S_11, VSWR, phase characteristics and observed effect of different parameter by changing height for all substrate in terms of return losses. The matching network of a Microstrip Patch Antenna at 5.4 GHz has designed and developed using the software Advanced Design System (ADS) momentum. A rectangular patch is used as the main radiator. This act as high radiator for low dielectric and low radiator for high dielectric.

Keywords- Advanced Design System (ADS)Momentum, Microstrip patch antenna, momentum simulation, Radiation Absorbent Material, Voltage standing wave ratio (VSWR).

1. Introduction

Antenna is used to transform RF signal, travelling on a conductor, into an electromagnetic wave in free space. Antennas demonstrate a reciprocity property. It means that an antenna will maintain the same characteristics regardless if it is transmitting or receiving. Microstrip Patch Antenna have been widely used in various useful applications, due to their low weight and low profile, Conformability, easy and cheap in realization.

In its most basic form, a Microstrip patch antenna consist of a radiating patch on one side of a dielectric substrate which has a ground plane on the other side. The patch is generally made of conducting material such as copper or gold that can take any possible shape. The radiating patch and feed line are usually photo etched on the dielectric substrate.

The electric field is zero at the center of the patch, The maximum (positive) at one side and Minimum (negative)on the opposite side.

It should be mentioned that the minimum and maximum continuously change side according to the instantaneous phase of the applied signal. The electric field does not stop abruptly at the patch’s periphery as in a cavity; rather, the fields extend the outer periphery to some degree. These field extensions are known as fringing fields.

1.1 Feed Techniques

Microstrip patch antennas can be fed by a variety of methods. These methods can be classified into two categories- contacting and non-contacting. In the contacting method, the RF power is fed directly to the radiating patch using a connecting element such as a microstrip line.

In the non-contacting scheme, electromagnetic field coupling is done to transfer power between the microstrip line and the radiating patch. The four most popular feed techniques used are the microstrip line, coaxial probe (both contacting schemes), aperture coupling and proximity coupling (both non-contacting schemes).

2.0 Microstrip Line Feed

In this type of feed technique, a conducting strip is connected directly to the edge of the microstrip patch.
The purpose of the inset cut in the patch is to match the impedance of the feed line to the patch without the need for any additional matching element. This is achieved by properly controlling the inset position. Hence this is an easy feeding scheme, since it provides ease of Substrate Ground Plane.

Fig:2 Top View of Antenna and generated Layout of schematic design of Microstrip Antenna

### 2.2 Method of Analysis (micro strip Patch antenna)

The rectangular patch antenna is approximately one-half wavelength long section of rectangular micro strip transmission line. When air is the antenna substrate. As the antenna is loaded with a dielectric as its substrate, the length of the antenna decreases as the relative dielectric constant of the substrate increases. The resonant length of the antenna is slightly shorter because of the extended electric "fringing fields" which increase the electrical length of the antenna slightly. The simple analytical modeling techniques are transmission model and cavity model.

In order to operate in the fundamental $TM_{10}$ mode, the length of the patch must be slightly less than $\lambda/2$ where $\lambda$ in the dielectric medium and is equal to $\lambda_0/\sqrt{\varepsilon_{\text{eff}}}$ where $\lambda_0$ is the free space wavelength.

**Line Calculation**

Line calculation for Patch in terms of length $L$, Width $W$, Height $H$, and characteristics impedance $Z_0$ at centre frequency 5.4GHz can be calculated by following calculation.

The initial design was done by hand to create a rough model in which to begin simulations. The equations are as listed below and sourced by:

\[
\varepsilon_{\text{eff}} = \left(\varepsilon_r + 1\right)/2 + \left(\varepsilon_r - 1\right)/2\left[1 + 12(h/W)\right]^{1/2}
\]  
\[
L = 0.814h \left(\varepsilon_{\text{eff}} + 0.3\right)(W/h + 0.264)/\left(\varepsilon_{\text{eff}} - 0.258\right)(W/h + 0.8)\}
\]  
\[
W = 0.5\lambda_0/\sqrt{\varepsilon_{\text{eff}} + 1}/2
\]

Characteristics impedance $Z_0$ are as:

When $W/H \geq 1$

\[Z_0 = 120\pi/\sqrt{(\varepsilon_{\text{eff}}(W/H)+1.393+2/3\ln((W/H)+1.444}]\]

Due to fringing fields, the change in dimensions of length is given by:

\[\Delta L = 0.412h \left[\varepsilon_{\text{eff}}(W/H)+0.264\right]/\left[\varepsilon_{\text{eff}}^2 - 0.258\right][W/H + 0.8]\]

Effective length $L_{\text{eff}} = L + 2\Delta L$

### 3.0 Design Specifications

The three essential parameters for the design of a rectangular Microstrip Patch Antenna are:

- Frequency of operation ($f_0$): The resonant frequency of the antenna must be selected appropriately. Hence the antenna designed must be able to operate in this frequency range. The resonant frequency selected for design is 5.4 GHz.
- Dielectric constant of the substrate ($\varepsilon_r$): The dielectric material selected for design are ($\varepsilon_r$) =2.22, 4.84, 9.6, 10.5. A substrate with a high dielectric constant has been selected since it reduces the dimensions of the antenna.
- Height of dielectric substrate ($h$): For the micro strip patch antenna to be used in cellular phones also, it is essential that the antenna is not bulky. Hence, the height of the dielectric substrate is selected as 2 mm. Hence, the essential parameters for the design are:
  - $f_0 = 5.4$ GHz
  - $\varepsilon_r = 2.22, 4.84, 9.6, 10.5$
  - $h = 2$ mm

### 3.1 Schematic of patch antenna

Equivalent circuit or schematic of micro strip edge feed Patch antenna of 5.4 GHz. Micro strip patch antenna in ADS platform is shown in screen snapshot, Figure3.

Fig:3 Overall Patch antenna schematics or equivalent circuit on ADS schematic window
Creating Layouts of the design for the microstrip patch antenna shown in fig.2 which is included all the necessary routes has transmission lines so the layout should be automatic and can be achieved by selecting layout->generate.

4.0 Simulation and Results Analysis

Principle of operation of ADS momentum based on Finite Difference Time Domain (FDTD).
After performing the simulation, the results window open, choose the Rectangular Plot and smith chart plot, select the respective parameter for frequency 5.4 GHz.

Summary of parametric study results for four Substrate \( (\epsilon_r=2.23, 4.84, 9.6,10.5) \) with Length \( , \) width and height at centre frequency 5.4 GHz in terms return losses \( S_{11}, \) VSWR and Smith chart for impedance matching \( Z. \) Where \( Z=Z_0/Z_0, \) should be unity.

<table>
<thead>
<tr>
<th>Antenna</th>
<th>( \epsilon_r )</th>
<th>L/W in mm</th>
<th>H in mm</th>
<th>( S_{11} ) in db</th>
<th>VSWR</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>2.23</td>
<td>19.12/21.88</td>
<td>2</td>
<td>22.329</td>
<td>1.191</td>
</tr>
<tr>
<td>2</td>
<td>4.84</td>
<td>13.62/16.25</td>
<td>2</td>
<td>14.581</td>
<td>1.635</td>
</tr>
<tr>
<td>3</td>
<td>9.6</td>
<td>9.17/11.94</td>
<td>2</td>
<td>12.76</td>
<td>2.113</td>
</tr>
<tr>
<td>4</td>
<td>10.5</td>
<td>8.57/11.58</td>
<td>2</td>
<td>8.99</td>
<td>1.774</td>
</tr>
</tbody>
</table>

Table:1

Simulated parameter \( S_{11}, \) VSWR, Smith chart or impedance matching \( Z_0/Z_0 \) and phase plot for \( \epsilon_r=2.22 \) on Display window shown in figure 4.

In the fig. 4, \( S_{11} \) is -23db, VSWR is 1.4 and \( Z \) is (.865 + 0.035) ohm at the frequency 5.44 GHz

Effect of changing height from 1.9mm to 5mm for the substrate material RT/Duriod 5870 with \( \epsilon_r=2.23 \) shown in table 2:

<table>
<thead>
<tr>
<th>S N</th>
<th>H in mm</th>
<th>freq. ( obtained-GHz )</th>
<th>( S_{11} ) in db</th>
<th>VSWR</th>
<th>( Z=Z_0/Z_0 )</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1.9</td>
<td>5.411</td>
<td>-15.623</td>
<td>1.4510</td>
<td>0.717-j.008</td>
</tr>
<tr>
<td>2</td>
<td>2.22</td>
<td>5.427</td>
<td>-23.046</td>
<td>1.210</td>
<td>0.895-j.109</td>
</tr>
<tr>
<td>3</td>
<td>2.78</td>
<td>5.418</td>
<td>-35.682</td>
<td>1.079</td>
<td>1.000-j.012</td>
</tr>
<tr>
<td>4</td>
<td>2.88</td>
<td>5.425</td>
<td>-64.314</td>
<td>1.164</td>
<td>1.017-j.062</td>
</tr>
<tr>
<td>5</td>
<td>3</td>
<td>5.463</td>
<td>-27.375</td>
<td>1.251</td>
<td>1.121-j.093</td>
</tr>
<tr>
<td>6</td>
<td>4</td>
<td>5.477</td>
<td>-17.501</td>
<td>1.467</td>
<td>1.364-j.067</td>
</tr>
<tr>
<td>7</td>
<td>5</td>
<td>5.485</td>
<td>-13.961</td>
<td>1.723</td>
<td>1.652-j.101</td>
</tr>
</tbody>
</table>

Table:2

Simulated parameter \( S_{11}, \) VSWR, Smith chart or impedance matching \( Z_0/Z_0 \) and phase plot for \( \epsilon_r=2.22 \) with height H= 2.78mm shown below in fig.5.

Figure 5 shows, \( S_{11} \) is -35.682 db at center frequency 5.43GHz, VSWR is 1.079 and impedance matching \( Z \) is (1.000-j0.120) ohm has been observed at frequency centered around 5.4GHz, which is used for WLAN application.

Figure: 4
The return loss $S_{11}$ is -13db with frequency band 5.343GHz-5.5456GHz and bandwidth of 120 MHz has been achieved with $\varepsilon_r = 4.84$.

**Conclusions**

In this paper by using basic design methodology, compared four different Substrate materials for designing of the microstrip patch antenna. The materials which has been taken with dielectric constant $\varepsilon_r = 2.23, 4.84, 9.6, 10.5$. It has been found from the above simulation results that the as Rogers RT/Duriod 5870 $\varepsilon_r = 2.23$ for $H=2.78mm$ gave good results in terms of different simulated parameter are given in table 1&2 and In the figure 4&5 with $S_{11}$ is -35.6db, VSWR is 1.079, $Z$ is 1.00Ω. This proposed design of microstrip antenna functioning for the WLL systems at 5.4 GHz has been obtained and analyzed.

Future aspect of this effective design of micro strip antenna (1) has to increase beam width and develop a microstrip patch array for the conformal antenna which is used for military application.

**References:**


**Authors Profile**

**Mrs. Brajlata Chauhan** received her Master degree in Digital communication from Uttarakhand technical University Dehradun Uttrakhand in 2010, India and also pursuing her PhD from UTU Dehradun UK. She is working in Dehradun Institute of Technology , Dehradun as AP. A life member of Institute of Electronics and Telecommunication Engineers India. She is having teaching experience of 07 years in Engineering Institutions.

Sandip Vijay received B.Sc. (Enng.) from PIT Patna (erstwhile Magadh University) in 1998 M.Tech. (Electronics & Comm. Enng.) in 2000, the member of IEEE(USA), NSBE (USA), IANEG(USA), ISOC (USA).Life Member of ISTE (INDIA) has published over Eighty research papers in national and international journals/conferences and IEEE proceeding publication in field of Wireless & Digital Communication Network, and supervised more than 40 projects/dissertation of M.Tech. & B.Tech. Students. He started his career as R & D Engineer in Dell Computers in the field of Quality Assurance Engineer then joined teaching profession as a Lecturer and later on promoted as Asst. Professor (EC) in GEIT, Uttarakhal Technical University, (Now Graphic Era University). He has successfully completed major research projects independently by VSI & DA-IICT, Gandhi Nagar, in the field of VLSI Design. He finished his Doctorate in 2011 from I.I.T. Roorkee in the...
field of Wireless Computing under Ministry of HRD, Government of India fellowship. Currently working as Associate Professor in Department of ECE & AEI at Dehradun Institute of Technology (DIT), an autonomous college of Uttarakhand Technical University, Dehradun (Uttarakhand). Total Publication Till Date: 82

Prof. (Dr.) S.C. Gupta, Professor Emeritus, IIT Roorkee, is having total experience of about 35 years as Prof. & Head Dept of Electronics & Communication IIT Roorkee, Now he is working in DIT Dehradun as Dean (PG Course) of Electronics and communication department.