

An SC-FDMA Channel Estimation Algorithm Research Based on Pilot Signals

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Abstract—SC-FDMA as LTE uplink main technology has been widely studied,at the same time, channel estimation around such technology is becoming hot.In order to research and increase channel estimation accuracy of LTE uplink system,an algorithm based on pilot is proposed in this paper,this algorithm’s main idea is,by setting a certain bit error rate,choosing some proper data after channel estimation done under R-LS and M-MMSE, combining the chosen data and former block pilot and increasing pilot number to do channel estimation again, system bit error rate and mean square error results can be got by simulation;at last,the theoretical analysis and simulation results show that this algorithm really can make the channel estimation accuracy improved and bit error rate decreased.

Keywords-channel estimation; block pilot; bit error rate; SC-FDMA;LTE

I. INTRODUCTION

OFDMA(Orthogonal Frequency Division Multiple Access) is not suitable for LTE(Long Term Evolution) uplink,because it asks for high power amplifier and transmitting power from transmitting terminal,such requirements can hardly be met for a mobile equipment whose direction is smaller,smarter and long battery life.SC-FDMA avoids OFDMA’s defects without changing OFDM transmission structure,what’s more,it combines traditional technologies of FDMA and single carrier transmission,and can let multiuser share communication resources.that’s why SC-FDMA has been chosen as LTE uplink main technology and widely used in wireless communication.

With the development of SC-FDMA,channel estimation around such technology is becoming a research focus.LTE study group has no firm rule on channel estimation methods, but has a rule that its uplink has massive pilot pattern.At present,commonly used pilot channel estimation algorithms include LS(Least Square),MMSE (Minimum Mean Square Error),and channel estimation based on transformation domain,etc.Literature[1] proposed a symbol decision algorithm for fast time-varying channel estimation,but its accuracy is not good enough;literature[2] presented a combination channel estimation algorithm that has good performance when SNR is between 7~15dB,but it decreases when SNR is other value;literatures[3-4] had a research on LS, MMSE and decision feedback algorithm,the simulation results show that MMSE has a better performance; TNMSE(Truncated Normalized Mean Squared Error) algorithm proposed in literature[5] overcomes sideband

effect, which is reflected by the limited length Fourier series approximation of Gibbs phenomenon, and shows that MMSE channel estimation can withstand minimum sideband recession;R-LS (Regularization-LS) algorithm and M-MMSE(Mismatch-MMSE) algorithm are respectively proposed in literatures[6-7],the feasibility of theoretical analysis is verified shown in its simulation results.The algorithm proposed in this paper is based on block type pilot and combines R-LS and M-MMSE,the simulation results show that higher channel estimation precision can be obtained through the limited time iterative estimation, which means that this algorithm is feasible.

II. SYSTEM MODULE

SC-FDMA based on OFDMA has a DFT module and an IDFT module respectively in its sender and receiving end.Signals after being modulated and coded are firstly transformed into multipath parallel signals through serial-parallel transformation,then go into subcarrier mapping after N points DFT;multipath signals become a serial signal after parallel-serial transformation and M points IFFT,then CP (Cyclic Prefix) is added on the signal,finally,such serial signal is transmitted into wireless channel after DAC(Digital to Analogue Conversion) and RF,receiving end has a nearly opposite process.System block diagram is shown as Figure.1

With normal CP,the basic frame structure of SC-FDMA in time domain is 10ms long and equally divided into 20 time slots,each slot is 0.5ms long and has 7 SC-FDMA symbols,in frequency domain,each slot has 12 sub-carriers and each subcarrier bandwidth is 15kHz,pilot signal location is shown as Figure 2,block type pilot locates in every slot’s forth symbol.At anytime t ,the received signals under multipath can be presented as:

$$y(t) = x(t) * h(t) + w(t). \quad (1)$$

here,* is convolution operation, $h(t)$ is channel shock response, $w(t)$ is additive noise, suppose the modulated signal is $x=[x_1,x_2,, x_N]T$,after N points DFT: $X=F_N \cdot x$,here, F_N is DFT matrix whose element is:

$$(F)_{n,m} = \frac{1}{\sqrt{N}} \exp(-\frac{j2\pi nm}{N}). \quad (2)$$

here, $n,m \in [0,N-1]$, N is the length of SC-FDMA symbol,the received signal is:

$$y = x * h + w. \quad (3)$$

CP turns linear convolution into circular convolution, and FFT changes circular convolution into frequency domain's product, represented as:

$$Y = XH + W. \quad (4)$$

here, Y is received signal, X is transmitted signal, H is frequency domain channel response, W is Gaussian white noise. The structure of SC-FDMA pilot is block type pilot that can be produced by ZC^[8-9] serial, a L_{zc} long ZC serial q_b is shown as:

$$q_b(s) = \begin{cases} \exp(-j2\pi b \frac{s^2/2 + sd}{L_{zc}}), L_{zc} = 2a \\ \exp(-j2\pi b \frac{s(s+1)/2 + sd}{L_{zc}}), L_{zc} = 2a + 1 \end{cases}. \quad (5)$$

here, a is natural number, $b \in \{1, 2, \dots, L_{zc}-1\}$ is ZC serial radical exponent, $s=0, 1, \dots, L_{zc}-1$, d is any integer, for simplicity, suppose $d=0$ ^[10], then signal with pilot is:

$$\hat{X} = [\hat{X}_1, \hat{X}_2, \dots, \hat{X}_N]^T. \quad (6)$$

after subcarrier mapping:

$$\bar{X} = A \cdot \hat{X}. \quad (7)$$

here, A is zero padding matrix:

$$A = \begin{bmatrix} 1 & 0 & \dots & 0 & 0 \\ & & \mathbf{0}_{v \times N} & & \\ 0 & 1 & & 0 & 0 \\ & & \mathbf{0}_{v \times N} & & \\ \vdots & \vdots & & \vdots & \vdots \\ 0 & 0 & \dots & 0 & 1 \\ & & \mathbf{0}_{(M-(v+1)N) \times N} & & \end{bmatrix}_{M \times N}. \quad (8)$$

here, $v \in \{0, 1, \dots, \lfloor M/N \rfloor\}$ and $M > N$, There are two subcarrier mapping ways of SC-FDMA: localized FDMA and distributed FDMA, their main difference is zero padding among sub-carriers, when inserting v "0" among each DFT sampling output signals, it's localized FDMA if $v=0$, and each user's bandwidth is $1/U$ of system bandwidth; it's distributed FDMA if $v > 0$, each user's bandwidth is expanded as long as system bandwidth; this paper adopts the former. Then, after M points IFFT, we get:

$$\bar{X} = F_M^H \cdot \hat{X}. \quad (9)$$

here, F_M^H is IFFT matrix, then, multiplying a stack factor C that means inserting CP to eliminate ISI (Inter Symbol Interference), then signal with CP is:

$$\tilde{X} = C \cdot \bar{X}. \quad (10)$$

$$C = \begin{bmatrix} \mathbf{0}_{L_{zc} \times (M-L_{zc})} & \mathbf{I}_{L_{zc}} \\ \mathbf{I}_M & \mathbf{0} \end{bmatrix}_{(L_{zc}+M) \times M}. \quad (11)$$

I is unit matrix, then keep going as Figure 1, SC-FDMA signals are produced and transmitted into wireless channel.

III. THE ALGORITHM'S BASIC IDEA

After a range of treatments, SC-FDMA symbols arrive at the receiving end, some symbols that have error bits and are under a certain threshold will be extracted and put into channel estimator with pilot for quadratic estimate, if necessary, more estimates can be done till achieving desired results. block diagram is:

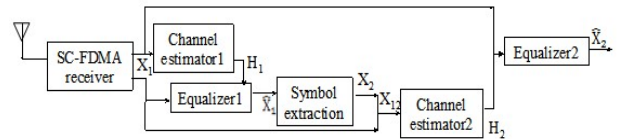


Fig 3. Algorithm block diagram

In Figure.3, channel response H_1 comes out from channel estimator1, we can get the first channel estimated and equalized result \hat{X}_1 by putting H_1 and SC-FDMA symbols in equalizer1 if channel estimation accuracy or BER meets the requirements, keep moving as Figure 1, if not, set a certain BER, extract some reliable signals from the first estimation result and put it in channel estimator2 with pilot, then, get the secondary channel estimation and equalization results \hat{X}_2 , more iterative computations can be made if necessary, the threshold BER comes from the relationship of BER and SNR^[11] shown as formula(12)-(16):

$$BER = \frac{1}{2} [1 - \frac{\mu}{\sqrt{2 - \mu^2}}]. \quad (12)$$

here, $\mu = SNR / (1 + SNR)$, Suppose X_n is transmitted signal, and Y_n is received signal, H_n is channel response in frequency domain, W_n is Gaussian white noise, the signal received on the n th subcarrier must be:

$$Y_n = X_n H_n + W_n. \quad (13)$$

after channel estimation, the signal and SNR are:

$$\hat{X}_n = Y_n / H_n = X_n + W_n / H_n. \quad (14)$$

$$SNR_{\hat{X}_n} = \sigma_n^2 / (\sigma_{W_n}^2 / H_n) = |H(n)|^2 SNR_x. \quad (15)$$

Obviously, SNR is function of channel grain $H(n)$, if $SNR_{threshold}$ is determined, and $|H(n)|^2 SNR_x \geq SNR_{threshold}$, what means:

$$|H(n)| \geq H_{threshold} = \sqrt{SNR_{threshold} / SNR_x}. \quad (16)$$

then, the threshold $H_{threshold}$ is determined, so the chosen symbol is proper as long as its grain is higher than this threshold.

IV. PERFORMANCE SIMULATION AND ANALYSIS

A. Theoretical Analysis

For LS algorithm, suppose the pilot symbol is g , $\tilde{\mathbf{H}}_g$ is $g \times 1$ Gaussian white noise vector, and its variance is $\sigma_{\tilde{\mathbf{H}}_g}^2$, then, the channel estimation $\hat{\mathbf{H}}_g$ is:

$$\hat{\mathbf{H}}_g = \mathbf{H}_g + \tilde{\mathbf{H}}_g = \mathbf{F}_g \mathbf{h} + \tilde{\mathbf{H}}_g \quad (17)$$

here, $g \in (0, \dots, G)$ is useful pilot, \mathbf{h} is $L \times 1$ channel shock response, L is channel maximum delay length, and $L \leq L_{cp}$, \mathbf{F}_g is $G \times L$ matrix, \mathbf{F}_L is $N \times L$ matrix which has the lines where pilot locates and the former columns of $N \times N$ DFT matrix, $\tilde{\mathbf{H}}_g$ is $G \times 1$ Gaussian white noise vector, then the whole channel shock response is:

$$\hat{\mathbf{H}}_{LS} = \mathbf{F}_L (\mathbf{F}_G^H \mathbf{F}_G)^{-1} \mathbf{F}_G^H \hat{\mathbf{H}}_G \quad (18)$$

as the reference literature [6] shows that parameters chosen in LTE system makes LS algorithm can not be directly applied to SC-FDMA, But classical and robust R-LS algorithm can. Here, expression $(\mathbf{F}_G^H \mathbf{F}_G)^{-1}$ is an ill-conditioned matrix because there are some unmodulated sub-carriers in SC-FDMA system, this problem can be resolved by adding a normalized matrix $\alpha \mathbf{I}_L$ to avoid inverting matrix, here α is a constant, usually, it is 0~1, the system has best overall performance when $\alpha=1$ ^[12], so the estimation performance can be improved in given SNR range, channel response in frequency domain is:

$$\hat{\mathbf{H}}_{R-LS} = \mathbf{F}_L (\mathbf{F}_G^H \mathbf{F}_G + \alpha \mathbf{I}_L)^{-1} \mathbf{F}_G^H \hat{\mathbf{H}}_G \quad (19)$$

its MSE (Mean Square Error) is:

$$MSE_{R-LS} = E[\|\hat{\mathbf{H}}_{R-LS} - \mathbf{H}\|^2] \quad (20)$$

simplified as:

$$MSE_{R-LS} = \sigma_{\tilde{\mathbf{H}}_g}^2 \mathbf{F}_L (\mathbf{F}_G^H \mathbf{F}_G + \alpha \mathbf{I}_L)^{-1} \mathbf{F}_G^H \quad (21)$$

For M-MMSE algorithm, its MMSE estimation result is:

$$\hat{\mathbf{H}}_{MMSE} = \mathbf{F}_L (\mathbf{F}_G^H \mathbf{F}_G + \sigma_{\tilde{\mathbf{H}}_g}^2 \mathbf{C}_h^{-1})^{-1} \mathbf{F}_G^H \hat{\mathbf{H}}_G \quad (22)$$

here, $\mathbf{C}_h = E[\mathbf{h}\mathbf{h}^H]$, MMSE is statistical estimation, which needs second order statistics (power delay and noise variance). To avoid second order statistical estimation and real-time inverting matrix that is for MMSE direct application, suppose channel power delay spectrum is evenly distributed, then, \mathbf{C}_h has identity matrix structure, expression (22) can be improved as:

$$\hat{\mathbf{H}}_{M-MMSE} = \mathbf{F}_L [\mathbf{F}_G^H \mathbf{F}_G + (\sigma_{\tilde{\mathbf{H}}_g}^2 / \sigma_G^2) \mathbf{I}_L]^{-1} \mathbf{F}_G^H \hat{\mathbf{H}}_G \quad (23)$$

and, its MSE is:

$$MSE_{M-MMSE} = E[\|\hat{\mathbf{H}}_{M-MMSE} - \mathbf{H}\|^2] \quad (24)$$

simplified as:

$$MSE_{M-MMSE} = \|\mathbf{R}_{HH} - \mathbf{R}_{HH} (1 + \frac{\beta}{SNR} (\mathbf{R}_{HH})^{-1})^{-1}\| \quad (25)$$

here, $\mathbf{R}_{HH} = E\{\mathbf{H}\mathbf{H}^H\}$ is autocorrelation matrix of channel response, then, the average SNR is:

$$SNR = E|\mathbf{X}(n)|^2 / \sigma_g^2 \quad (26)$$

and:

$$\beta = E|\mathbf{X}_{12}(g)|^2 / E|1/\mathbf{X}_{12}(g)|^2 \quad (27)$$

here, β is a constant that is dependent on modulation, $\beta=1$ when the modulation is QPSK.

B. Simulation Results

Simulation in this paper is based on LTE uplink SC-FDMA, for simplicity, $U=2$, carrier bandwidth is 5MHz, FFT sampling points $M=512$, $L_{cp}=M/8$, Doppler frequency shift is 50Hz, subcarrier number is 300, subcarrier mapping is localized FDMA, modulation is QPSK, bit error rate threshold is 0.005, twice iterative computations, 1000 times of Monte Carlo simulation on each SNR node, channel is typical city macro cell model, simulation results are shown as Figure 4 and Figure 5.

Figure 4 shows the MSE performance curves of two kinds of estimation methods, under the condition that channel diameter number is known or estimation is accurate and exists error, whether M-MMSE or R-LS, performance of second estimation result is better than that of first estimation, and M-MMSE performance has about 2~3 dB gain than R-LS algorithm, each estimation method has no error basement, estimation performance gains of both have obvious rising trend when SNR growing, but that trend slows down when SNR is higher.

Figure 5 shows system BER curves of two kinds of estimation methods, when SNR is low, estimation accuracy can not be improved because the signals chosen from estimator1 are not enough, but estimation precision rises when the selected symbol number and SNR increasing, obviously, result from estimator2 is better than that from estimator1, and estimator2 of R-LS is 5dB better than estimator1, such two algorithms nearly have error basement when SNR is close to 18dB, grains of such two algorithms have almost linear change and have no obvious rise when SNR is higher,

V. CONCLUSION

Each user in SC-FDMA system occupies only part band and sub-channel has time-frequency related properties, an SC-FDMA channel estimation based on pilot is proposed, from the above theoretical analysis and simulation results, such channel estimation algorithm is feasible in LTE uplink system, because it not only improves channel estimation precision, but also reduces system BER. The only defect is that such algorithm becomes complex when making iterative

computations, the actual hardware design feasibility and economy efficiency may not be obvious, so, how to improve estimation precision and reduce the algorithm complexity at the same time is still a problem that is worth studying.

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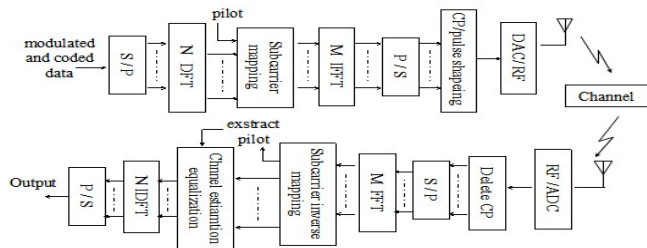


Fig 1. SC-FDMA transmit-receive system(include pilot)

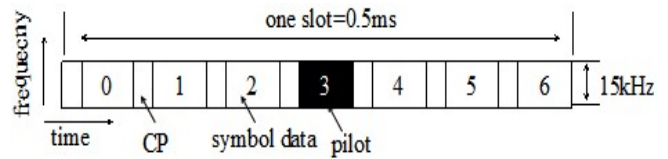


Fig 2. SC-FDMA pilot signal location(include normal CP)

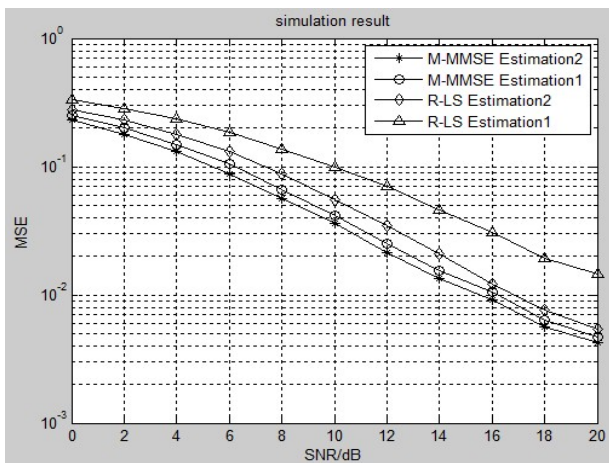


Fig 4. MSE performance curves of two kinds of estimation methods

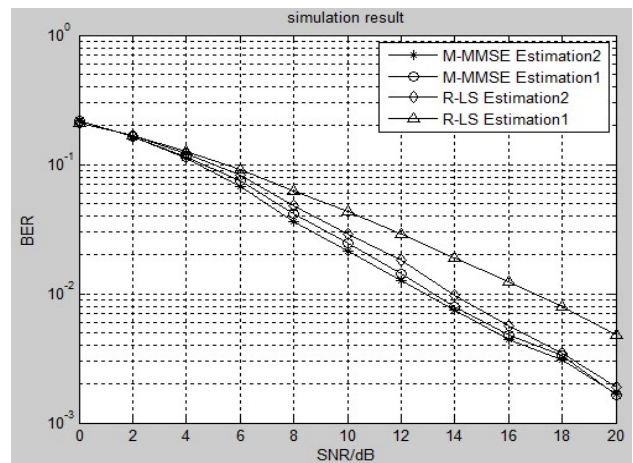


Fig 5. System BER curves of two kinds of estimation methods