A reflective TWTA predistortion linearizer for Ka-band application

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Abstract—A predistortion linearizer used for Ka-band travelling wave tube amplifiers (TWTA) was proposed. Based on analysis of nonlinear transfer characteristics of TWTA using a power series, the inverse nonlinear distortion characteristics of predistorter which was also the model of predistortion linearizer was proposed. A reflective structure was used to realize the linearizer, it’s composed of anti-parallel schottky diodes, quadrature hybrid, matching networks seen from the diodes. The Carrier-to-Intermodulation(C/I) of Ka-band TWTA was improved a maximum of 19dB by proposed predistortion linearizer.

II. BASIC PRINCIPLE AND SIMULATION

A. nonlinearity of TWTA

When exciting a TWTA working at its nonlinear region, the output spectrums contain the original signal as well as some undesired new frequencies. This is called nonlinear distortion, which is produced by a loss of linearity. The input-output transfer response of a TWT amplifier can be represented by a power series

\[ V_{out}(t) = k_1 V_{in}(t) + k_2 V_{in}^2(t) + k_3 V_{in}^3(t) + \cdots + k_n V_{in}^n(t) \]  

where \( V_{in}(t) \) is the input signal, \( V_{out}(t) \) is the output signal and \( k_n \) the amplitude of \( V_{in}^n(t) \). When the TWTA works at its linear region, \( k_n = 0 \), \( n \geq 2 \), otherwise, \( k_n \) is incompletely equal to 0. In case of weak nonlinearity, the first three terms are enough to characterize output signal.

When a single-carrier signal, e.g. a cosine wave signal, is substituted into this expression, in which

\[ V_{in}(t) = A \cos \omega_1 t \],

the original cosine wave as well as its harmonic distortion products will be contained in the output waveform. Unless the bandwidth is greater enough than an octave or more, these harmonics can be filtered by an extra band pass filter. However, when excited by a signal with more carriers, e.g.

\[ V_{in}(t) = A \cos \omega_1 t + \cos \omega_2 t \]

intermodulation-distortion products are not neglectable. Then

\[ V_{out}(t) = k_1 V_{in}(t) + k_2 V_{in}^2(t) + k_3 V_{in}^3(t) + \cdots + k_n V_{in}^n(t) \]

Parts of the intermodulation-distortion terms, \( V_{out} \), is commonly known as third order intermodulation products,
are so close to the carrier signals that could not be filtered and would affect the linearity.

B. principle of predistortion

Predistortion linearizers are widely used in microwave and satellite application due to that they are relatively simple and that they are independent to TWTA that could be external components and reduce the design complexity. The predistortion linearizer(Fig. 1) can be thought as a black box whose I/O transfer characteristic is strictly opposite to that of the TWTA to be linearized. In other way, a predistorter is an IMD generator, the IMDS by which are equal in amplitude and 180 degree out of phase with the IMDs generated by the TWTA. So when cascaded together, the whole I/O transfer characteristic will satisfy the need for lossless transmission.

Figure 1 schematic diagram of predistortion linearizer

Using a simple power series model to describe the predistortion linearizer, we get

\[ V_{out}(t) = g_1 V_{ip}(t) + g_2 V_{ip}(t) \] (3)

where \( V_{ip}(t) \) and \( V_{out}(t) \) represent the input and output signal of the PD respectively, \( g_n \) is the coefficient of \( V_{out}(t) \). Supposed that \( V_{ip}(t) = B(\cos \omega_1 t + \cos \omega_2 t) \), signals contained in \( V_{out}(t) \) are the carrier signals( \( \omega_1 \), \( \omega_2 \) ), IMD signals( \( 2 \omega_1 - \omega_1 \), \( 2 \omega_1 - \omega_2 \)), Substituting \( V_{out}(t) \) into \( V_{out}(t) \) as input signals of the TWTA, it can be taken as a pair of two-tone signals ( \( \omega_1 \), \( \omega_2 \) and \( 2 \omega_1 - \omega_1 \), \( 2 \omega_1 - \omega_2 \)). Then output signals are the combination of two-tone signals ( \( \omega_1 \), \( \omega_2 \) ) together with their IMD signals ( \( 2 \omega_1 - \omega_1 \), \( 2 \omega_1 - \omega_2 \) ) and two-tone signals ( \( 2 \omega_2 - \omega_1 \), \( 2 \omega_1 - \omega_2 \) ) together with their IMD signals( \( 5 \omega_1 - 4 \omega_2 \), \( 5 \omega_2 - 4 \omega_1 \)), signals at other frequencies could be filtered out. It can be proved that there would be no other signals generated at the frequencies of \( 2 \omega_1 - \omega_1 \) and \( 2 \omega_1 - \omega_2 \). So the third order IMD of the TWTA can be written as follows

\[ V_{out} = \frac{3}{4} B^4 (g_1 g_2 + g_1 k + \frac{9}{4} k k) g B^4 + \frac{27}{4} k k_2 g_2 B^4 \] (4)

\[ + \frac{9}{4} k k_2 g_2 B^4 [\cos (2 \omega_2 - \omega_1) t + \cos (2 \omega_1 - \omega_2) t] \]

and the expression of carrier signals of \( V_{out}(t) \) is

\[ V_C = (k_1 + 1) (g_1 B + \frac{9}{4} g_1 k B^3) + \frac{3}{4} k_2 (g_1 B + \frac{9}{4} g_1 k B^3) \]

\[ + \frac{3}{4} k_2 g_2 B^3 (g_1 B + \frac{9}{4} g_1 k B^3) \cos(\omega_1 t + \cos \omega_1 t) \]

When designing a predistortion linearizer, two constructions should be followed: 1. The circuit can generate IMD(this is why in some conditions the predistortion linearizer is called IMD generator); 2. The minimum cancellation of signals at carrier frequencies (this can bring less insertion loss). Based on these two principles and according to equation (3) and (4), we can get \( g_1 \) and \( g_2 \).

Figure 2 shows the model of one predistortion linearizer. It is composed of a hybrid, whose input \( 1 \) and output \( 3 \) ports are the input and output ports of predistorter respectively. To barrier the DC current from bias of the diodes, DC blocks are utilized. The other two ports(2, 3) of the hybrid which are 90° out of phase connect the compensation circuits and comprises a pair of antiparallel diodes severing as nonlinear elements and impedance adjusting unites to adjust the impedance seen by the antiparallel diodes.

Supposed that the hybrid is working at its ideal type, according to its S parameter matrix, we have

\[ V_{out}(t) = -j \Gamma V_{ip}(t) \] (6)

where \( \Gamma \) is the reflection coefficient seen from port 2 or 3 to the IMD generator. Here the antiparallel diodes are equal to a variable resistance, hence the impedance network and the bias voltage both influence complex resistance, and then change \( \Gamma \). So to change the magnitude and phase of the IMD is to change the impedance network as well as the bias voltage. When designing a PD, first get the model parameters of the PD from the nonlinear characteristics of the TWTA according to equation (1) to (4). Second calculate the characteristic impedance and electrical angel of the microstrip stub serving part of the impedance network together with the load resistance. Then adjust the bias voltage to optimize the nonlinear characteristic produced by this PD. The advantage of this type of PD is that the I/O impedance characteristic would not be impacted by the nonlinear elements, and it has a small VSWR.

Figure 2 the model of the predistortion circuit

C. realization of the key elements of PD

To realize the PD, microstrip DC block to isolate DC signal, 90° hybrid, microstrip bias circuit are needed.
1) microstrip DC block

Using a microstrip DC block to isolate DC signal is to simplify the whole structure, improve the stability, and minimize the parasitic parameters due to some capacitance elements. Interdigital coupled microstrip line can meet those needs within a broad bandwidth. Fig. 3(a) is a microstrip DC block whose equivalent circuit is a double series stubs Fig. 3(b). The $[Z]$ matrix of the circuit in Fig. 3(b) is as follows, where $z_{o0}$ and $z_{0e}$ means odd and even characteristic resistance respectively. $[S]$ matrix is then derived from equation (7), and its bandpass characteristic meets the needs as a microstrip DC block.

\[
[Z] = \begin{bmatrix}
z_{11} & z_{12} \\
z_{21} & z_{22}
\end{bmatrix} = \begin{bmatrix}
\frac{-z_{0e} + z_{0o}}{2} & -j \frac{z_{0e} - z_{0o}}{2} \csc \theta \\
-j \frac{z_{0e} - z_{0o}}{2} \csc \theta & \frac{-z_{0e} + z_{0o}}{2}
\end{bmatrix}
\]

(7)

Fig. 3 microstrip DC block and simulation result

2) microstrip bias circuit

A microstrip sector bias is used to provide DC current for the diodes. Simulation in Advanced Design System (ADS) of Agilent shows that, radius $R$, angle $\theta$ and characteristic resistance $w$ of the sector together with the electrical angle $L_1$ of the high resistance line between the sector and the main transmission line are the key factors to be optimized.

Fig. 4 Bias circuit and its simulation results

3) 90° hybrid

90°hybrid (Fig. 5(a)) is a kind of narrow-band coupler, and the characteristic resistance together with its electrical angel of the branch line are the key factors. Fig. 5(b) is the simulation results for the hybrid working at the frequency band of 29GHz and 31GHz.

Fig. 5 quadrature hybrid and simulation results

D. simulation design of the PD

In order to validate the proposed predistortion linearizer circuit, system simulation is done. The substrate is Rogers 5880 with the dielectric constant of 2.2, tan delta of 0.0009 and thickness of 0.254 mm. The simulation results are illustrated in Fig.6 and Fig.7.

AM/AM and AM/PM characteristics of TWTA over a dynamic range of 26dB (from its saturation point to backoff
of 20dB) are -5.6dB and -42° respectively. Insertion loss of the PD together with the small signal drive amplifier and attenuator is 0dB at linear region. And AM/AM and AM/PM characteristics of PD are 4.8 dB and -32° respectively.

Fig. 6 Simulation line-up of linearized TWTA

Fig. 7 Simulation results of linearized TWTA

III. EXPERIMENT RESULTS

Carrier to intermodulation distortion ratio(C/I) is a parameter used to characterize magnitude and phase distortion in amplifier systems. Here we use carrier to 3rd intermodulation distortion ratio(C/IM3) to show the nonlinear distortion of the linearized TWTA. Fig.8 is the measurement system and Fig.9 is the results. It is clear that, when cascaded with the proposed predistortion linearizer at the input port, C/IM3 of the TWTA (backoff 3dB from saturation) at the upper and lower sideband of central frequency is 27.04 dBc and 27.89dBc respectively. That means the C/IM3 improvement of the TWTA with and without external linearizer is 13dB. The maximum improvement is 19dBc when backoff 6dB from saturation.

Fig. 8 measuring of the linearized TWTA

Fig. 9 test results of C/IM3

IV. CONCLUSION

Based on detailed analysis of nonlinear characteristics of TWTA and its predistortion linearization, a new linearizer working at the Ka band is proposed. This linearizer is composed of 90° hybrid and nonlinear diodes together with microstrip bias as well as microstrip DC block. Cascaded two-tone experiments of TWTA and proposed linearizer show that the design is effective. Compared with similar design[10], by tuning bias voltage, impedance stub and load resistance, this simple structure can flexibly modify the nonlinear characteristic, and greatly improve TWTA nonlinearity, and the realization is to some degree easy. Besides, conclusion from theoretical analysis can do some guide to similar design.

REFERENCES
