Software Simulation Method for Digital IF GNSS Signal

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Abstract—Data simulation is an important auxiliary method for baseband signal processing algorithm design of digital IF GNSS receiver. Especially when the high sensitivity and high dynamic receivers are developed, or multipath and cross correlation mitigation techniques are considered, exact and complete tests under all kinds of necessary scenes can be carried out with the IF signal data which is generated by software simulation method. With this method, the developing efficiency can be improved and the cost can be cut down. In this paper, precise model of the IF signal is given firstly through analyzing GNSS receivers’ RF processing flow. Then, considering the discrete characteristic of software processing, the IF signal simulation flow and block of the simulation software are designed. Finally, the specific design process is given through a design example. When the IF signal simulation software is used for testing baseband signal processing algorithms, GNSS receivers’ IF signal data under every necessary test scene can be generated just by parameter adjustment.

Keywords—GNSS receiver; Digital IF signal; Software simulation; Spectrum aliasing; Band limiting filter

I. INTRODUCTION

In the signal processing flow of intermediate frequency (IF) GNSS receiver, the signal acquisition and tracking performance determines the precision of pseudo-range directly, and it is an important factor which affects the positioning, speed measuring and timing performance [1][2]. Baseband signal processing algorithms for high sensitivity, high dynamic receiver and multipath, cross correlation mitigation techniques have gained much attention in the field of high performance GNSS receivers’ research [3]-[6]. Software simulation is an important auxiliary method in baseband signal processing algorithms’ developing. It uses computer to implement all (or part of) the functions of baseband signal processing, so both the signal parameter adjustment and the statistical analysis of simulation results can be carried out very conveniently.

The software simulation method is much cheaper than the special IF signal collection hardware equipment. Besides, this method can evaluate the frequency and phase tracking performance and BER of navigation data bits conveniently, so it is more practical and effective. Common signal processing algorithms often ignore the code Doppler frequency, the influence of IF filter and the sampling effect. But these factors can influence high sensitivity, high dynamic signal processing and multipath, cross correlation mitigation algorithms sensitively. This paper focuses on the software simulation of GNSS receivers’ IF signal. Processing flow and block of the simulation software is given, and it is illustrated concretely through a design example.

II. IF SIGNAL MODEL

The structure of GNSS receiver is illustrated in Fig. 1. The received signal is amplified by the preamplifier, and then mixed with the signal generated by local oscillator. After filtered by the band pass filter, the received signal is converted to IF. The bandwidth of the band pass filter is determined by requirement of the baseband signal processing algorithms. The output of the A/D converter is feed to digital baseband signal processing unit, in which the navigation data is demodulated and the pseudo-range, pseudo-range rate measurements are taken through signal acquisition and tracking process. At last, the position, velocity and time (PVT) determination and navigation filtering are implemented in a microprocessor generally.

![Figure 1. Top block diagram of digital IF GNSS receiver](image)

All necessary clock signals are derived from the reference oscillator by the frequency synthesizer, base on the frequency plan of the receiver design. Because the reference oscillator has frequency error \( \delta f(t) \), the local oscillator and sampling signal also have frequency error denoted as \( \delta f_{LO}(t) \) and \( \delta f_{sam}(t) \) separately. The mathematic model of the RF front end output analog IF signal is

\[
r(t) = \sum_{i=1}^{N} A_i \cdot D_i(t + F_{\text{IF}}(t)/f_T - \tau_{i}) \cdot \cos(2\pi f_{T}\tau + \theta_{i}) + n(t)
\]

where \( A_i \) is the signal amplitude, \( D_i \) \((1 \text{ or } -1)\) is the navigation data bit, \( C_i \) is the pseudo random noise (prn) code, \( f_T \) is the nominal frequency of transmitted carrier by satellites, \( \tau \) and \( \tau_r \) are the phase delay of data bits and prn code respectively. \( F_{\text{IF}}(t) \) is the integration of Doppler frequency \( f_d(t) \), and...
\( F_d(t)/f_c \) denotes the Doppler effect influenced on navigation data rate and prn code rate. \( f_c \) is the receiver’s nominal IF frequency. \( \theta_d(t) \) is the carrier phase offset introduced by the carrier frequency error \( f_c(t) = f_c(t) - \delta f_c(t) \), and \( \theta_c \) is the initial phase of the received carrier. The first item of the above equation is summation of \( N \) received satellites’ signal. \( n(t) \) is zero mean Gaussian white noise with two sides spectrum power density \( N_0/2 \) bandwidth 2\( B \), which is centered at the frequency \( f_{IF} \).

The carrier-to-noise power ratio (CNR) is an evaluation of the signal quality degradation due to noise. RF front end noise figure (NF) is used to describe the degradation by RF processing. When the room temperature equals to 290 K, the IF noise spectrum power density is \( N_0 = k_B T_0 NF = -204 \text{ dBW/Hz + NF dB} \). The definition of CNR is

\[
\text{CNR} = \frac{A^2}{2 N_0}
\]  

The sampling frequency must satisfy the bandpass sampling theorem, and should be determined in the frequency plan, considering design requirements of RF signal down conversion, the choice of \( f_{IF} \), design of frequency synthesizer and baseband algorithms [8]. The processing loss caused by quantification is related to the IF signal bandwidth, bit width and threshold setting of the quantification. The research conclusions about this question are given in [9], so the non-quantified discrete IF signals generated by software can be used for simulating baseband signal processing algorithms directly, and the quantification loss should be considered at last.

III. FLOW AND STRUCTURE OF SOFTWARE SIMULATION

The flow of IF signal simulation is similar to the navigation signal generation on satellite [7]. Firstly, the navigation data bits are spread by prn code. Second, the spread signal is filtered by a bandpass filter to limit its bandwidth. Then the filtered signal is modulated by carrier. More detailed processing flow and structure are determined by the values of the parameters in the signal model and their relationship. The nominal IF frequency \( f_{IF} \), the IF signal bandwidth 2\( B \) and the nominal sampling frequency \( f_s \) are constant, and they are determined by the frequency plan. The influence of reference oscillator frequency error on the IF carrier frequency offset and phase of each sample point is determined by reference oscillator frequency and phase control can be obtained easily by using prn code cycle counter as its phase controller. Both the code and carrier NCO are driven by frequency \( f_c \). Phase increment of adjacent points is determined by frequency of the signal which is to be generated.

IV. DESIGN EXAMPLE

In this section, the GPS L1 C/A code IF discrete signal simulation is taken as an example to illustrate the calculation method of design parameters and the structure of each module in Figure 2.

A. Determination of \( f_{LO} \), \( B \) and \( f_c \)

\( f_{LO} \), \( B \) and \( f_c \) are derived from the frequency plan considering the receiver’s reference oscillator frequency and the requirement of RF, baseband processing. In this paper, the frequency plan of a typical GPS RF product is adopted.
According the plan, nominal frequency of the reference oscillator is \( f_r = 16.367667 \text{MHz} \), and local oscillator frequency \( f_{LO} \) is 96 times of \( f_r \). \( f_{LO} \) is equal to 1423968 Hz. \( B \) is set to 1.023 MHz. Considering the band pass signal sampling theorem and design convenience of the frequency synthesizer, \( f_s \) is set to 5455889 Hz.

### B. Determination of \( F_s \), \( \sigma^2 \), and \( A \)

Spectrum aliasing is inevitable in generating sampled prn code sequence (1 or -1) by software. To mitigate the aliasing, prn code sequence is firstly generated with high sampling frequency \( f_s \), which is 20 times of the prn code rate. It is ensured that the main lobe of the spectrum do not alias with the first to ninth lobe. Secondly the sequence is filtered by low pass filter to restrict the bandwidth, and then output of the filter is decimated to the IF sampling frequency. For convenient sampling, \( f_s \) can be set as 4 times of the real IF sampling frequency. If the real frequency of the receiver’s oscillator is 9 Hz smaller than its nominal value, \( f_s \) should be set to 4\( f_r \) = 21823544 Hz. This value is slightly larger than 20 times of the prn code rate, and it can simplify the decimation.

When zero-mean Gaussian noise is generated by random number generator, the noise variance \( \sigma^2 \) is the input parameter. Because the noise bandwidth is half of the sampling rate \( f_s \), its single side power spectral density is \( 2\sigma^2 f_s \). After modulated by IF carrier with unit amplitude, power spectral density of the IF noise becomes \( N_0 \sigma^2 / 2 f_s \). Thus the IF signal amplitude can be derived from the CNR definition as follow

\[
A = \sqrt{10^{\text{CNR} / 10} \sigma^2 / f_s}
\]  

### C. Carrier and Code NCO Control

Driving clock frequency \( f_c \), phase increment NCO delta and the width of the phase accumulator \( N \) are three basic parameters of NCO. The relationship between them and the generating signal frequency \( f/s \) is

\[
\text{NCO \_delta} = 2^N f / f_s
\]  

The code frequency is \((1023000+ f_{ch}(t) \times 1540) \text{Hz}\). Obviously, according to equation (4), real-time control of the generating code rate can be achieved based on the carrier Doppler frequency model. Phase delay \( \tau \) corresponds to the initial value of the phase accumulation register in code NCO. One bit navigation data contains 20 code cycles. When cycle counter is used to control the scheduling of the navigation data, its Doppler characteristic will be consistent with the code’s, and the bit boundary can always be aligned with a code periodic boundary. The sub-millisecond part of phase delay \( \tau_{\text{ms}} \) corresponds to the code phase delay \( \tau_c \), and its millisecond part is implemented by setting the code cycle counter to the corresponding initial value. The carrier frequency of the IF signal is \( f_{IF} + f_{ch}(t) - \delta f_{LO}(t) \). The local oscillator frequency error \( \delta f_{LO}(t) \) can be obtained by multiplying the reference frequency error with the ratio \( f_{LO}/f_r \). The initial carrier phase can be set arbitrarily. Frequency of the carrier used for modulating noise is \( f_{lo} \).

### D. FIR Low Pass Filter Design

Carrier Doppler frequency of the received signal is usually within -5kHz ~ 5kHz, so the cutoff frequency of its pass band can be set to 1.5MHz according to the former discussed design principles of low pass filter bandwidth. Linear phase FIR low pass filter can be designed by window function method [10], and the Hann window with minimum stop band attenuation of -44dB is used. To ensure the steepness of the transition zone, the filter is set to 200th order, and the transition bandwidth is \( 3.1 \times F_s / 200 = 338265 \text{Hz} \). Figure 3 shows the amplitude and phase-frequency response curve of the FIR low pass filter within the frequency band of 0 ~ 2MHz.

![Amplitude and phase-frequency curves of FIR LPF](image)

### E. FIR Band Pass Filter Design

The FIR band pass filter before the decimator is used to simulate the anti-aliasing filter before A/D conversion in receiver. Its central frequency is \( f_{IF} \), and bandwidth is \( 2B \). The linear phase FIR band pass filter can also be designed using Hann window function. Its order is set to 200, and cutoff frequency is set to \( f_{IF} + B = 3.1 \times F_s / 400 = 5.3161 \text{MHz} \) and \( f_{IF} - B = 3.1 \times F_s / 400 = 2.9318 \text{MHz} \). Figure 4 shows the amplitude and phase-frequency response curve of the filter within the frequency band of 0 ~ 8MHz.

The final generated IF sampling signal is the output of the 1/4 decimator. The IF signal generated by software simulation and its spectrum are shown in Figure 5 (a) and (b) separately, where \( \sigma^2 = 10000 \) and \( CNF_{IF} = 45 \text{dBHz} \). The sampling rate of the final generated signal is \( F_s / 4 = 5455886 \text{Hz} \). Central frequency of the signal is \( f_{IF} \), but due to spectrum shift caused by the decimation the signal’s spectrum band center in Figure 5 appeared at \( \pm (F_s / 4 - f_{IF}) = \pm 1331918 \text{Hz} \).
V. CONCLUSIONS

The precise mathematical model for IF signals is established based on the analysis of the RF processing for the digital IF GNSS receiver. The flow for IF signal simulation with software is designed, regarding the relationship among the parameters in the established model and the characteristics of digital processing. The detailed design method of the software modules are also demonstrated. The time domain and frequency domain characteristics are heavily investigated in the simulation method design. With a few modifications of the related parameters, the simulation method can be applied to most baseband signal processing algorithms.

When dealing with the more complicated processing algorithm, considering the frequencies and phases relationship among different satellites and the similarity between some sub-frames of the navigation message, it is demanded to generate the navigation message, phase delay and Doppler frequency shift precisely based on the location, motion and signal arrival time of the receiver in addition to the satellite constellation orbit model. This will be the key research point on satellite constellation emulator in the next step.

REFERENCES