Reconstruction of the Computer Hardware Curriculum

Ding SHEN, Fang LI and Sheng-ju SANG
School of Information Science and Technology
Taishan University
Taian Shandong, China, 271000
Corresponding author:sang1108@163.com

Yue-ying WU
School of Mathematics and Statistics
Taishan University
Taian Shandong, China, 271000
wyy7758525@126.com

Yue-ying WU
School of Information Science and Technology
Qingdao University of Science and Technology
Qingdao, Shandong, China, 266061
Corresponding author:wyy7758525@126.com

Abstract—This paper demonstrates a thoroughly innovative research on reconstruction of the curriculum of computer hardware. The curriculum practice, to some appropriate extent, brings together ‘topical case studies’, ‘class projects’, and ‘study and engineering work in industry’ activities throughout the teaching progress. This research provides a broad background in computer engineering concepts and enhanced teaching practice that could meet requirements from a wide range of industries and organizations.

Keywords—Reconstruction; computer hardware; curriculum system.

I. INTRODUCTION

The original course hierarchy is not suitable for the goals of all colleges [1,2]. Foreign universities, like MIT, UC Berkley, Stanford, MCUs have setup computer curriculum with special features [7-9]. The Computer Science and Technology Teaching Steering Committee of the Ministry of Education of China proposes the direction of computer disciplinary development with the spirit of “classified educational specification” and encourages universities providing various types of curriculum plans[3] based on both social exceptions and available resources[4,5]. More and more university in China have made some progress on such kind of research and brought forward practice plans on curriculum, contents and laboratories [6].

II. CURRICULUM OF COMPUTER HARDWARE

The goal focuses on computer applications to meet real engineering activities which not only include broad theories, but problem-solving techniques in engineering. According to this goal, this paper optimizes the original courses ‘Computer Organization’, ‘Principle of Mini-computer’, ‘Principle of MCU’, ‘and Computer Architecture’ in undergraduate phase; setup both the teaching arrangement and curriculum for computer hardware knowledge base. Lecture hours allocation is shown in table 1.

III. KNOWLEDGE STRUCTURE OF COMPUTER HARDWARE COURSE

The optimization of this curriculum is exemplified with below changes in the course of ‘Principle of Computer Organization’ and ‘Mini-computer Interfaces technology’.

In the course of ‘Principle of Computer Organization’, there are detailed explanations on microcode working principles, structures, design of logics inside controllers and accumulators. These are basic knowledge for processor designer and researchers, but for computer applications, who cares at most about processor features and programmable registers, the internal structure details could be seen as a ‘black box’.

And in the teaching practice of ‘Mini-computer Interface Technologies’, the 8086/8088 assembly programming could be simplified; the legacy parallel interface chips learning could be shorten; for important concepts like DMA, just focuses on its principles; and for other chipset features, only a brief introduction. These changes impacts little on the undergraduates’ ability preparations as the legacy parallel chips and 8086/8088 assembly are almost unavailable in real applications. We could put the bus, memory related contents into the 8051-chip sub-system context; not only for its simplicity, but straight forward and easy to understand and grasp the spirits.
### TABLE I. LECTURE HOUR’S ALLOCATION OF COMPUTER HARDWARE COURSE

<table>
<thead>
<tr>
<th>Courses</th>
<th>Type</th>
<th>Credit</th>
<th>Class hours</th>
<th>Semester</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>Total</td>
<td>Theory</td>
</tr>
<tr>
<td>Introduction to Computer Science</td>
<td>Compulsory</td>
<td>5</td>
<td>80</td>
<td>44</td>
</tr>
<tr>
<td>Computer Circuit</td>
<td>Compulsory</td>
<td>5</td>
<td>96</td>
<td>78</td>
</tr>
<tr>
<td>Computer system architecture (1)</td>
<td>Compulsory</td>
<td>4</td>
<td>72</td>
<td>54</td>
</tr>
<tr>
<td>Computer system architecture (2)</td>
<td>Compulsory</td>
<td>4</td>
<td>72</td>
<td>54</td>
</tr>
<tr>
<td>Computer network</td>
<td>Optional</td>
<td>3</td>
<td>54</td>
<td>36</td>
</tr>
<tr>
<td>Principle of MCU</td>
<td>Optional</td>
<td>3</td>
<td>54</td>
<td>36</td>
</tr>
<tr>
<td>Computer system design</td>
<td>Optional</td>
<td>3</td>
<td>54</td>
<td>36</td>
</tr>
<tr>
<td>Embedded system</td>
<td>Optional</td>
<td>3</td>
<td>54</td>
<td>42</td>
</tr>
<tr>
<td>Internet of things technology</td>
<td>Optional</td>
<td>3</td>
<td>54</td>
<td>42</td>
</tr>
<tr>
<td>Computer Comprehensive Design</td>
<td>Compulsory</td>
<td>4</td>
<td>72</td>
<td>20</td>
</tr>
</tbody>
</table>

### IV. CONTENTS OF TEACHING IN COMPUTER HARDWARE CURRICULUM

Some contents appear in multiple courses in computer hardware and teachers usually explain them from the perspective of each course. This forms the fact that for one knowledge unit, many teachers mentioned, but no one gives a clear description, which makes students feel dull and negates learning interests.

A. **Construction of Computer Hardware Basics Course**

‘Computer Hardware Basics’ integrates knowledge’s units from courses ‘Circuit Basics’, ‘Analog Circuit’ and ‘Digital Circuit’. By taking this course, students are expected to grasp basic concepts of circuits and electrical quantities and elements, as well as the analysis and design of simple electronic circuits using appropriate techniques.

These contents encompass basics in hardware circuits and make solid foundation for the counterpart in later courses ‘Principle of Computer Organization’, ‘Mini-computer Interface Technologies’ and ‘Computer Architectures’ to give students a full view of VLSI digital logic circuits and the goal of preparing employment-oriented professional skills for undergraduates.

During the concrete practice, we abandoned and augmented some contents based on professional features and preparation goals, tried our best to fulfill the practice of ‘Computer Hardware Basics’ to make knowledge applied.

B. **Optimization of ‘Computer Architectures’ Course**

This course integrates contents in original ‘Computer Organization’, ‘Assembly Programming’, ‘Principle of Mini-computer’, ‘Computer Architecture’ and extends application practices. This course introduces basic concepts of computer ALU, host controller, memory and I/O; also the memory hierarchy, bus, instruction set, interrupt and assembly programming including common I/O. The whole course could be categorized into two parts: Computer Architectures one (CA1) and Computer Architectures two (CA2).

### V. ELECTIVE CURRICULUM IN COMPUTER HARDWARE

To meet various personal interests of students and the profession realm, the undergraduate computer hardware courses should also include ‘Principle of MCU’, ‘Embedded System Design’, ‘Computer Organization’, ‘Computer Network’, ‘Internet of things technology’, etc. These courses could enhance students’ cognition of computer system and setup a better foundation for future career development planning.

A. **Principle of MCU**

MCU technology is taken seriously by colleges and industries. This course, as an important elective in computer related department, the teaching spirit of which is: the goal of focusing on basic knowledge to prepare for practice and utilization of such knowledge, the creativity and acquiring more during such a process.

B. **Embedded System Design**

It is embedded as part of a complete device often including hardware and mechanical parts. Embedded systems control many devices in common use today, from music player to astronaut satellite systems. Setting up this course could not only provide a...
chance for students to get in touch with more advanced computer architecture trends, but enhance their problem-solving abilities with real embedded system products.

C. Internet of Things Technology (IoT)

With the background of other computer hardware courses, students could exercise in the data labeling, acquisition, transmission, recognition and control, also the practice of system integration and IoT engineering which could be beneficial for them to become professionals with high-caliber that could adapt to the development of those strategic emerging industries.

D. Computer Organization Design

This course inspects and evaluates the design and function allocations in a particular computer system and its performance indicators.

E. Computer Network

This course introduces basic theories of network technology and common network devices which was separated into different courses before. TCP/IP network stack is a main part of contents. The learning outcome is to master the setup and maintenance techniques of LAN and Internet and some practical networking techniques.

VI. SUMMARY

The new ‘Computer Hardware Basics’ and ‘Computer Architectures’ simplify some complex concepts and give students a easy-to-understand whole picture of computer, also with new technologies in this area. Meanwhile, according to students’ interest and professional realms, the elective courses like ‘Principle of MCU’, “Embedded System Design”, etc. are setup. The reconstruction could be used extensively and it could bring enhanced cognitions of computer system, more practice in system integration and application and better preparation for qualified professionals that could adapt to emerging industries.

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