Abstract—Based on the Hilbert transform algorithm, a digital phase detector for sinusoidal signal is proposed. A half-band IIR filter implemented by the all-pass method approximates the Hilbert transform to realize the orthogonal decomposition of the signal. The Prosthaphaeresis formulas and Cordic algorithm are used to deal with the phase difference. The method can effectively avoid the influence of signal amplitude and frequency fluctuation on the phase detection results, and improve the precision of phase detector. The phase detector is designed and verified on Xilinx’s Artix7 FPGA. Experiments show that the phase detector error is 0.3%.

Keywords-hilbert; phase detector; FPGA

I. INTRODUCTION

Phase-locked loop(PLL) as an important circuit module in the circuit system plays an important role [1]. Widely used in sensor’s signal processing, wireless communication systems, high-speed interface and other fields. The phase detector(PD) as an important part of PLL, its phase accuracy and speed directly determine the performance of PLL, thereby affecting the performance of the entire system.

Hilbert transform is an important tool in signal analysis and processing, and is used to construct analytical signals in signal systems. Hilbert transform can decompose the signal into two mutually orthogonal signals without affecting the amplitude of its spectral components. This paper is the use of this feature to achieve the signal orthogonal decomposition, to lay the foundation for the signal processing following.

II. REALIZATION THEORY OF PHASE DETECTOR

The structure of phase detector(PD) is shown as Figure 1. The input signal can be converted into two orthogonal signals by Hilbert transform. For the transformation of the four-channel signal using the Prosthaphaeresis formulas as equations (1) (2), we can get

\[ A_1 A_2 \sin(\theta) \quad A_1 A_2 \cos(\theta) \]

The phase difference \( \theta \) can be obtained by the Cordic algorithm's cartesian coordinates to the polar coordinate transformation [2].

Hilbert transform is a set of mathematical equations that reflect the relationship between the real and imaginary parts of the signal Fourier transform (or amplitude and phase). The ideal Hilbert transform is defined as a DTLTI system with frequency response as (3) [3].

\[ A_2 \sin(\omega t + \theta + \varphi) A_1 \cos(\omega t + \varphi) - A_2 \cos(\omega t + \theta + \varphi) A_1 \sin(\omega t + \varphi) \]

\[ = A_1 A_2 \sin(\omega t + \theta - \omega t) \]

\[ = A_1 A_2 \sin \theta \]

Figure 1. The structure of phase detector
\[ A_2 \sin(\omega t + \theta + \varphi)A_1 \sin(\omega t + \varphi) + A_2 \cos(\omega t + \theta + \varphi)A_1 \cos(\omega t + \varphi) \]
\[ = A_1 A_2 \cos(\omega t + \theta - \omega t) \]
\[ = A_1 A_2 \cos \theta \]

\[ H_m(e^{j\pi f}) = \begin{cases} -j, & 0 \leq f < \frac{1}{2} \\ j, & \frac{1}{2} \leq f < 0 \end{cases} \]  

Equation (3) can be derived by Fourier transform,
\[ H(n) = \begin{cases} 0, & n \text{ is even} \\ \frac{1}{2n\pi}, & n \text{ is odd} \end{cases} \]  

For \( n \) is not absolutely feasible, the ideal Hilbert transform is non-causal, so can't be achieved physical [4]. The Hilbert transform in engineering applications are implemented using function approximation methods. Common methods are using FIR, IIR and other methods to approximately. Compared to FIR filters, IIR can have fewer orders and better statistical characteristics, so this paper uses IIR filters to achieve Hilbert transform.

The main features of the Hilbert transform are,
\[ F_{Hp} = 1 - F_{Hs} \]
\[ |H_H(e^{j\pi f})|^2 = \frac{1}{2} \]
\[ \Delta_{Hp} = \Delta_{Hs} \Leftrightarrow a_{Hp} = 10\log_{10}\left(1 + \frac{1}{10^{s/10} - 1}\right) \]  

Where,
\[ \Delta_{Hp} = 1 - \left|H_H(e^{j2\pi f_{Hp}})^2\right| \]
\[ \Delta_{Hs} = \left|H_H(e^{j2\pi f_{Hs}})^2\right| \]

Considering the main features of the elliptical half band IIR filter are,
\[ F = \frac{1}{2} - F_s \]
\[ |H(e^{j\pi f})|^2 = \frac{1}{2} \]
\[ \Delta_p = \Delta_s \Leftrightarrow a_p = 10\log_{10}\left(1 + \frac{1}{10^{s/10} - 1}\right) \]  

Where,
\[ \Delta_p = 1 - \left|H(e^{j2\pi f_p})\right|^2 \]
\[ \Delta_s = \left|H(e^{j2\pi f_s})\right|^2 \]  

Equation (5) and (7), we have,
\[ H_H(z) = H(z / e^{j\pi /2}) = H(-jz) \]  

It can be seen from the equation (9) that the transfer function of the hilbert transform can be obtained by substituting \( z \) in the elliptic half band IIR filter transfer function \( H(z) \) with \(-jz\). The full-pass implementation of the half-band IIR filter and the implementation of the hilbert converter are shown in and respectively.

The transfer function of IIR filter is,
\[ H(z) = \frac{1}{2}(H_u(z) + H_s(z)) \]  

Where,
Replace \(z\) by \(-jz\) in the equation (10), then the transfer function of Hilbert transform is,

\[
H_H(z) = H_{Ha}(z) + jH_{Hb}(z)
\]

Comparing equations (11) and (13) can be found that the coefficient of the Hilbert converter is the coefficient of the negative half-band IIR filter. So the essence of the design Hilbert converter is consistent with the design of half band IIR filter.

In this paper, the design objective of the Hilbert converter is \(SH = \{FHs, As\} = \{0.53, 46\}\), then the index of the half band IIR filter is \(S = \{0.28, 46\}\). When the order is \(n = 9\), the filter coefficients are shown in TABLE I.

![Figure 4. Hilbert transform implemented by 9-order elliptic complex IIR filter](image)

**TABLE I. FILTER COEFFICIENT**

<table>
<thead>
<tr>
<th>Coefficients((\beta))</th>
<th>Half Band IIR Filter</th>
<th>Hilbert transform</th>
</tr>
</thead>
<tbody>
<tr>
<td>(\beta_2)</td>
<td>-0.1206</td>
<td>0.1206</td>
</tr>
<tr>
<td>(\beta_3)</td>
<td>-0.6628</td>
<td>0.6628</td>
</tr>
<tr>
<td>(\beta_4)</td>
<td>-0.3900</td>
<td>0.3900</td>
</tr>
<tr>
<td>(\beta_5)</td>
<td>-0.8906</td>
<td>0.8906</td>
</tr>
</tbody>
</table>

III. **MODEL AND SIMULATION OF PHASE DETECTOR**

The hilbert transform simulation model in Simulink is shown as Figure 5. Figure 6.shows its bode.
As can be seen from Figure 6, at a frequency between 148Hz to 2kHz, the output \( y_i \) and \( y_r \) phase difference is constant at 90°, and its amplitude gain is almost 0dB, so it can achieve 90° phase shift function, and can’t be affected by frequency within a certain range. The phase shift effect is shown in Figure 5, where \( y_i \) is exactly 90° from \( y_r \). Figure 6 shows the simulation of phase detector. Input two sinusoidal signals, 
\[
x_1 = \begin{cases} 
\sin(\omega t + 0.3), & 0 \leq t < 18ms \\
\sin(\omega t + 0.1), & t \geq 18ms 
\end{cases}
\]
the response time is 2ms. After the system is stable, phase error is less than 0.15%.
IV. DESIGN AND TEST OF PHASE DETECTOR ON FPGA

Using verilog hardware description language to achieve the design of the various modules in the modelsim in the test, and ultimately downloaded to the FPGA to verify the experiment.

A. Hilbert Transform Test

The coefficients in Table 1 were quantized by 11 bits. The results are shown in Table 2. The input data is 18 bits, taking into account the impact of the truncation error, the calculation uses 22 bits to ensure the output accuracy. Modelsim simulation shown in Figure 9. It can be seen from the figure, the converter output of the two signal's phase difference is $90^\circ$. Frequency is the same as input sinusoidal signal.
TABLE II. QUANTIZATION TABLE OF HILBERT TRANSFORM COEFFICIENTS

<table>
<thead>
<tr>
<th>Coefficients(β)</th>
<th>Hilbert transform</th>
<th>Hilbert converter quantization</th>
</tr>
</thead>
<tbody>
<tr>
<td>β₁</td>
<td>0.1206</td>
<td>00011110111</td>
</tr>
<tr>
<td>β₂</td>
<td>0.6628</td>
<td>10101001110</td>
</tr>
<tr>
<td>β₃</td>
<td>0.3900</td>
<td>01100011111</td>
</tr>
<tr>
<td>β₄</td>
<td>0.8906</td>
<td>11100100000</td>
</tr>
</tbody>
</table>

Figure 9. Hilbert transform timing simulation diagram

B. Phase Detector Test

Download the code into FPGA for intermodulation, using two signals which are created internal FPGA that have the same frequency and phase difference which is 0.3. The phase discrimination results of the phase detector are shown in Figure 10. Skewing is a 32-bit signal that converts the decimal fraction to 0.30132. Phase error is 0.3%, which can meet the requirements exactly.

Figure 10. Result of phase detector

V. CONCLUSIONS

Based on the Hilbert transform algorithm, this paper presents a digital phase detector for sinusoidal signals. The phase detector system is designed and simulated in matlab, and using verilog hardware description language to achieve it. Finally, Xilinx's Artix7 FPGA is used as a platform to experiment and verify the design. The experimental results show that the phase detector can realize the phase discrimination of the sinusoidal signal, and the phase discrimination result is not affected by the amplitude fluctuation of the signal.

REFERENCES