Design of step counting system of multi-function

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Abstract. This design is mainly based on single chip as a step counting system control core, the overall system contains two modules: the transmitter end and the receiver. The transmitter comprised of a regulating circuit and a wireless communication module, the receiver can be divided into four small modules, respectively, the wireless communication module, keys module, clock circuit module and display module. Walking distance can be calculated by the size of the stride multiplied by the number and the size of the stride can increase or decrease by a key module implementation. Transmitter using mercury switch as the sensor, the use of mercury switch every tilt to control on-off circuit, implementation steps, steps of virtual circuit instead of walking. After collecting the data through the two wireless modules between serial data transceiver, finally sent to the display screen is displayed. This design is based on single chip design, more intelligent, easy to use, and easy to carry. Then in design for future upgrade maintenance, and add functionality, convenient for later to lay a good foundation.

1. Overall Design

The designed system is composed of wireless transmitting terminal and receiving terminal. The wireless transmitting terminal mainly comprises voltage stabilizing circuit, sensing part and wireless communication module. The wireless receiving terminal mainly comprises wireless communication module, clock circuit module, key circuit module and LCD display module. Firstly the mercury switch at transmitting terminal substitutes the number of steps, corresponding to the actual number of steps. One pulse is realized when pressing once the key, representing the step number, which is transmitted wirelessly via wireless communication module to be processed at receiving terminal. The LCD module displays the step number, and calculates walking distance according to set step size.

To put it simply, NRF24L01 is just a monolithic wireless transceiver chip manufactured by NORDTC and provided with four work modes of transmitting, receiving, idle and power failure, wherein the latter two work modes consume very low power. 1.9V to 3.6V voltage is required when chip is transceiving. The current consumption is 11.2mA in transmitting and 12.5mA in receiving. NRF24L01 has fine performance in performance parameters, small volume, and is compatible with other wireless modules, have power functions of automatic re-transmitting and automatic answering into the bargain. Hence it is popular in a great many application areas, such as: wireless sensor, wireless remote control, thief-proofer, intelligent movement device, etc.

Figure 1 Real object of NRF24L01
2. Hardware circuit design

After defining the scheme and elements needed, schematic is drawn with Protel99 software, and be sure to pay close attention to packaging problem in drawing. The package of some elements cannot be found in package library but need to be measured and drawn by oneself. After simulating successfully, draw PCB, and consider the reasonableness of general layout before wiring. The line size should be proper.
(1) LCD1602 display circuit

1602LCD liquid crystal display is driven by 5v voltage, has backlight function and can display two lines with 16 characters for each line. The Chinese characters cannot be displayed. The pins of this display in circuit is as shown in figure 11. The backlight for screen is sometimes burned out when just electrified. To cope, a 1k resistor can be connected beside pin 15 in series to prevent too high voltage. The brightness of screen can be adjusted via varistor.

(2) Voltage stabilizing circuit

As the wireless module uses voltage of 1.9V to 3.6V, while our power supply is 4.5V, we need to convert 4.5V voltage to be within 3.6V. Then we can adopt a voltage-stabilizing chip for this purpose.

(3) Wireless module

The design of wireless module functions to connect transmitting terminal and receiving terminal in series. The NRF24L01 belongs to monolithic radio frequency type, a novel type. On chip is configured many devices [6]. NRF consumes extremely low power, with power consumption for receiving slightly lower than that for transmission.

3. System program design

The software design for SCM should consider following problems:

(1) Highlight concrete functional requirements of this software design, divide system software design into several small and independent modules, to construct a correct general frame, thereby making programming simple with clear clue.

(2) Form a good programming habit, such as adopting structurization, modularization and subroutinization, etc., which are of great help to later debugging and modification;

(3) Create an accurate mathematical model to improve system performance via simulation software and get the parameters needed by design;

(4) Drawn up diagram for program flow;

(5) Reasonably distribute and utilize system resources;

(6) Add notes to program to facilitate reading by oneself;

(7) Avoid software design being disturbed by too many factors as far as possible to guarantee system reliability.

4. Debugging and result

After defining system scheme, I divided the main work into several steps: the first step is to design schematic using Protel99 software, then execute PCB; the second step is to simulate software design system; the third step is to install Keil C language programming to simulate control of SCM. When nothing is wrong with simulation result, the originally programmed program can be imported to SCM, then power is connected to examine whether the designed work achieves scheduled target.

5. Summary

Widely applied in intelligent domains, SCM can facilitate people's life significantly. Thus to master SCM knowledge appears especially important for our students majoring in communication engineering. However, the SCM knowledge is mostly deep and hard to be easily mastered, but needs to be delved into and learned. I believe it is not impossible to become proficient after long-term accumulation, even if we cannot completely apply it with ease.

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