Research on new energy controllable rectifier based on DSP
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Abstract. Three-phase PWM rectifier of New Energy based on DSP has been put forward by the disadvantages of random fluctuation about New Energy and the deficiency of the traditional rectifier. The Mathematic model of three-phase PWM rectifier in the d-q rotation frames is analyzed at first, to decouple the three-phase system, the current feed-forward compensation based on synchronous rotation frames was used, the current model decoupled was equal to one-order system and realized the separated control on the active and inactive current. The Space Vector Pulse Width Modulation (SVPWM) scheme is applied into the control of three-phase PWM rectifier and decoupling control has been realized by DSP TMS320F28027. The experimental results showed the feasibility of the design scheme of the system.

1. Introduction
After entering in twenty-first Century, the trend of worldwide energy shortage is more and more obvious, the energy crisis has triggered a series of global problems, the solution is only one, that is to develop new forms of energy and new energy saving technology combining the basis to establish a new type of clean, safe and sustainable energy system. New energy because of its no pollution, renewable, widely distributed characteristics such as being more and more accepted and recognized, but there are also disadvantages: long random energy fluctuations when fluctuations (diurnal or seasonal cycles), short-term fluctuations (hour level) and instantaneous fluctuation (seconds or less), with intermittent power supply instability, so how to implement the rectification of new energy is still a big problem: the traditional thyristor phase controlled rectifier has defects of low power factor and harmonic; although the diode rectifier input current fundamental no lag, the displacement factor is close to 1, but the harmonic current is great. According to the above description of the problem, we propose TMS320F28027 new energy controlled rectifier and control technology of the design and implementation scheme based on solving the problems of the big harmonic content, not energy feedback and low power factor rectifier circuit in the traditional sense, as a new energy conversion and control technology, the breakthrough of the technical warning.

2. Control strategy of three phase VSR
With the wide application of PWM rectifier in high performance power electronic devices, in order to achieve higher performance requirements, the control strategies have been launched, mainly around the implementation of unit power factor, reduce switching loss and improve the dynamic response and efficiency of several aspects of the system. In the design of control system of three-phase VSR, double closed-loop control system with voltage loop and current loop and outer voltage loop is $U_d$ DC voltage control of three-phase VSR, and given the active current of $i_d^*$; the role of the current network side current tracking command current, the power factor of the unit or controlled. As shown in Fig.1.
3 system hardware design

3.1 Main circuit structure and action analysis of three phase PWM rectifier

The main circuit topology of three-phase PWM rectifier circuit as shown in Fig.2, the left half of the three-phase PWM rectifier, the right part is the zero voltage switching circuit (ZVS), and the switch devices are connected in parallel with the buffer capacitor.

Since the switching frequency rectifier is much higher than the frequency of the grid, so in a switching period can be considered as the input current and output current of the rectifier is constant, which can be used to represent the constant current source and the input current and output current, so it can be used Fig. 3as the equivalent of Fig. 2.

In Fig.3, \(S_{reg}, D_s, C_{r1}\) respectively, on behalf of the rectifier power switch, freewheeling diode and snubber capacitor. Because the power switching device of the upper and the lower bridge arm of the three-phase rectifier bridge is always communicated with one side,\(\), So, \(C_{r1} = 3C_{sa}\). The soft switching section consists of two switching devices \(S_{a1}\) and \(S_{a2}\). Two diodes and \(D_1\) and \(D_2\), Resonant inductor \(L_r\) and resonant capacitor \(C_{r1}, C_{r2}\). \(C_{r1}\) is the main resonant capacitor in the soft switching structure, \(C_{r2}\) is an auxiliary capacitor used to reduce the resonant inductor \(L_r\) current reverse \(i_{Lr}\). During the main resonant capacitor \(C_{r1}\) is zero, the switching device of three-phase bridge can be operated to achieve zero voltage operation, which greatly reduces the power consumption.
3.2 Design of driving circuit for main switch device

Provided between the main circuit and control circuit interface circuit, it can according to the control requirements of the switching device applied to open or turn off signal, and the power electronic devices work in the switch state ideal, shorten switching time, reduce switching loss. In addition, some protection measures are often located in the drive circuit, or through the driver circuit.

The design of the full control device using the MOSFET, the opening of the gate voltage can not exceed the range of 12V~20V, when the gate voltage is 0, in the cut-off state. We choose HCPL3180 to build the MOSFET driver circuit, the circuit diagram shown in Fig. 4.

![MOSFET drive circuit](image)

Drive HCPL3180 high speed optocoupler AVAGO, DSP and electrical parts (MOSFET) isolated in order to protect the DSP, output current up to 2A, the better the advantages of HCPL3180 input side does not need power supply, HCPL3180 input drive diode minimum current 10mA, maximum current 16mA, voltage 1.5V, so the input current limiting resistor of 100Ω.

3.3 Design of voltage and current sampling circuit

The current and voltage are detected by Holzer sensor, because the output voltage signal, so the current and voltage signal processing can use the same structure of the conditioning circuit. Sample conditioning circuit shown in Fig. 5.

![Sample conditioning circuit](image)

In the design of the rectifier, the AC current signal and the voltage signal of the load side are needed to be sampled. Sampling of the current, the use of L07P020D15 Holzer current sensor; voltage sampling, using the BJHVS5-25A Holzer voltage sensor. Whether Holzer or Holzer as the voltage sensor, current sensor, its output was weak current signal into a voltage signal through the resistor R50 through the resistor, capacitor component of the filter and the phase ratio of the measured signal conditioning amplifier, the input voltage 0~3.3V to A/D DSP, and then through the rear end of the clamp circuit to DSP the input end of the A/D.

3.3 Design of zero crossing detection circuit

The zero crossing detection plays two roles in the system: (1) the frequency of the grid voltage is measured; (2). These two are the necessary conditions for the system to track the grid voltage and the same frequency with the input current, so it is very important to design the zero crossing
The zero crossing capture circuit is used to capture the zero crossing point of the AC voltage of the network side, and the output signal is supplied to the CAP1 port of the DSP. The program of the software phase locked loop is used to calculate the frequency and phase information of the AC voltage by calculating the time interval between the two pulses. This circuit consists of Venturi filter circuit, zero crossing comparator circuit and voltage regulation circuit.

The venturi filter circuit based on resonant principle, the minimum of the fundamental impedance and phase shift, including two parts of low-pass filter, high pass filter. R1 and C1, the composition of the low pass filter, low pass filter can filter out the harmonics of the input signal in the grid, the waveform is improved, but the phase had a lag, so the introduction of high pass filter R2, C2 compensation. As can be seen from the circuit, the high pass, low pass filter with identical topology, and RC phase symmetrical distribution, you can just ahead of the high pass filter phase delay offset low-pass filter. The filtered results after two times, not only to filter the harmonic sinusoidal, and no phase shift. In order to get a better phase signal R3, C3 and R4, C4 empathy.

4.Conclusion

Under the condition of uncontrolled rectifier A phase voltage and current waveforms as shown in Fig.7 (yellow for the current waveform, blue for the voltage).

Under the condition of controlled rectifier A phase voltage and current waveform (yellow for the current waveform, blue for the voltage) as shown in Fig. 8.

By comparing Fig. 7 and Fig. 8, in the rectifier conditions, three-phase current waveform distortion, non sine wave, PWM rectifier mode, current waveform adjustment for sine wave, and
voltage waveform phase, to achieve the desired purpose;

References:


