

Research on over current switch-off simulation of IGBT level in parallel connection

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Abstract: IGBT chips are basic structure of power electronic devices in the power grid system. The IGBT module with high current capability needs multi chips parallel connection. Because there are different performance parameters of the IGBT chips in the Wafer, it is necessary to select the parameters of the IGBT chips for the IGBT modules to make the performance parameters of chips as consistent as possible. However, it is unknown that what the influence of performance parameter differences between IGBT chips on the overall performance of the module such as IGBT modules' switch-off and RBSOA characteristics is. In addition, switching state of each single IGBT chip is not monitored when the whole module is on working state. This paper is trying to demonstrate a method to research the multi parallel chips on the performance of different parameters effects of each single IGBT chip switch-off Process, which is based on the T-CAD software tool.

Introduction

For high-power applications, rated currents of power devices usually have several thousand amperes unit. To realize such a large rated current, many IGBT chips, typically more than 10 and even more are connected in parallel. In an ideal case the current capability of IGBT chips scales with the number of chips connected in parallel. A perfect current sharing is not realistic because of a never completely matched impedance of each chip connection and parameter variations between the different chips. In addition, unequal performance parameters of the semiconductor devices can lead to further current imbalance in and between the IGBTs. However, the research literature on the parallel characteristics of IGBT chip and the related problems are few. When the IGBT chip once sealed module, it's difficult to monitor the single chip of IGBT module in the working state, especially switch-off state. If the chip performance parameters is not unequal, it will reduce the whole IGBT module's RBSOA and robust[1]. This paper is trying to provide a method to research the effects of different parameters of each IGBT chips on the performance of the multi parallel chips in switch-off process.

Device structure and simulation spice setup

The device under simulation is a 1.2 kV/75 A NPT IGBT. As shown in Fig.1, the active area of device of is 1cm² and the starting material is N-type float zone silicon, using the plane-gate structure. In addition, Substrate thickness is from 160um to 200um.

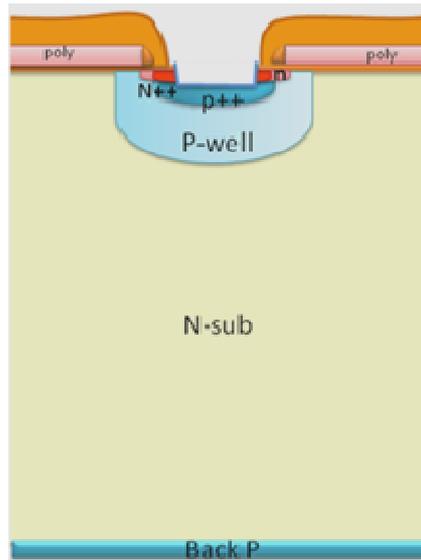


Fig 1. 1.2 kV/75 A NPT IGBT Chip Profile structure.

The simulation set-up with three IGBTs in parallel strings is shown as Fig.2. The mutual connections among electrodes of the three IGBT chips are implemented using the device structure editor software T-CAD. And Simulation of the switch-off process of three IGBTs devices is achieved by the timing and electrical characteristics of editing control. All the time series and the electrical characteristics of the control are added to the bus terminal in parallel with the three IGBT chips. So it can be used to simulate the three IGBTs as a whole module in the switch-off state. At the same time, it is possible to reduce the parasitic parameters and avoid the effects of the IGBT device itself. The purpose is to obtain the pure device performance parameters in the switch-off situation[2].

Simulation conditions:

- $T_{vj} = 25\text{ }^{\circ}\text{C}$,
- $I_C = 450\text{A}$,
- $V_{CE} = 600\text{V}$,
- $V_{GE} = \pm 15\text{V}$,
- $R_G = 6\Omega$.

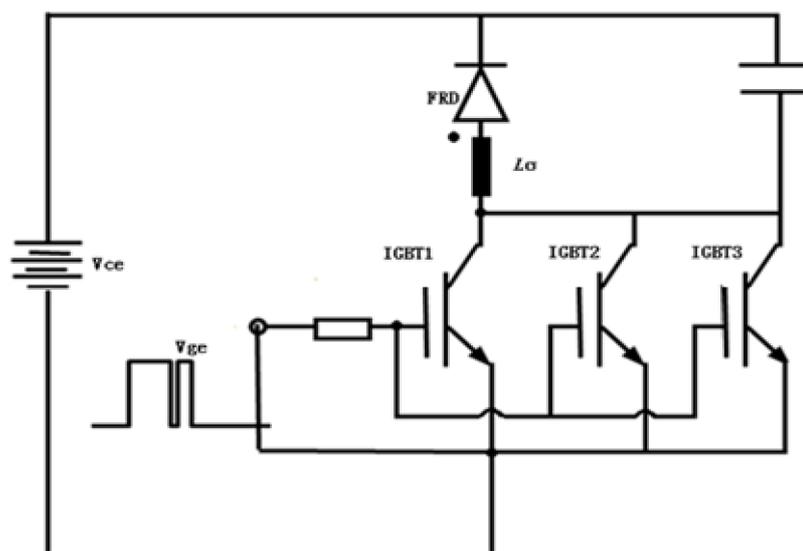


Fig 2. Simulation set-up with three IGBTs in parallel strings.

Results and discussion

The simulation of the 2 times of the rated current switch-off characteristics (RBSOA) for the 3 parallel IGBT chip is carried out in this paper. The simulation of the parallel chip is carried out Module stray inductance.

The module stray inductance is typically in the range of 50 nH[3] for classical module designs. For more advanced packages, this value is reduced to 10–20 nH[3]. The difference parameters of the IGBT device are shown in Table 1, and the other parameters are identical. Fig.3 shows the influence of Module stray inductance parameters on switch off current of the three parallel IGBT chips.

Table 1. The three parallel IGBT chips parameters

Parameter	P-well concentration	Back-P+ concentration	Vce.sat	Vth
IGBT1	105% A	105% B	2.576	5.931
IGBT2	A	B	2.592	5.907
IGBT3	95% A	95% B	2.608	5.882
Unit	/cm ³	/cm ³	V	V

According to the simulation results, the increase of stray inductance leads to the increase of peak voltage as well as the IGBT induced current [4]. Besides, The current of the IGBT1 is twice more than the rated current, when the stray inductance is significantly increased. It is likely to cause IGBT1 over current failure, thereby destroying the whole module [5].

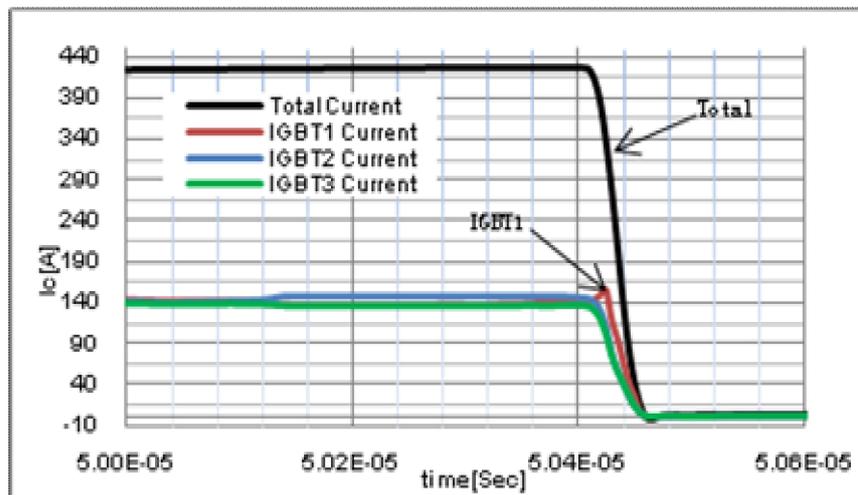


Fig 3. The influence of Module stray inductance parameters on switch off current of the three parallel IGBT chips, $L_s=20\text{nH}$.

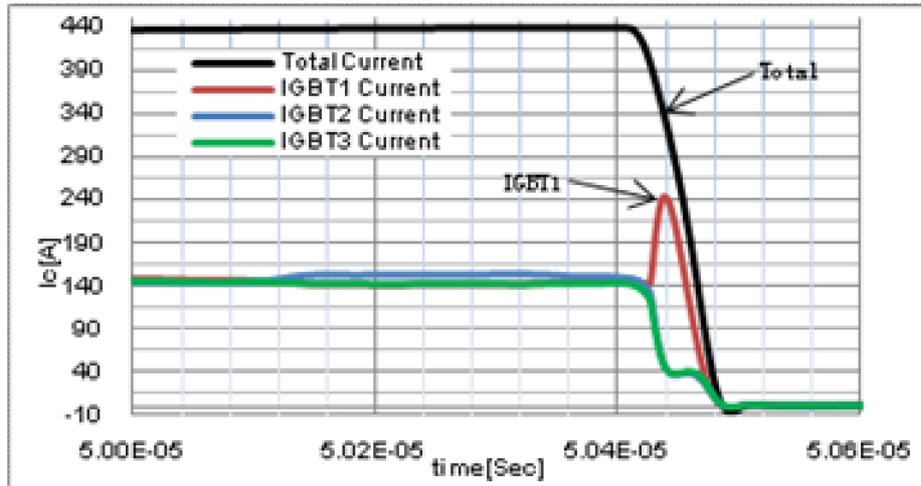


Fig 4. The influence of Module stray inductance parameters on switch off current of the three parallel IGBT chips, $L_s=50\text{nH}$.

Conclusions

In summary, the simulation results of the double rated current switch-off characteristics (RBSOA) for the 3 parallel IGBT chips are carried out in this paper. The simulation mainly included this aspect: Module stray inductance. The corresponding results were shown as figures, which could help select chips in wafer Level for high current module package.

Acknowledgements

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