A New Perspective for Understanding Data Flow Analysis

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Abstract. Data flow analysis is a technique that is widely used in compilers and program analysis tools. A lot of practical problems use data flow analysis, e.g., live variable analysis. The usual way of solving data flow analysis problem is to solve for the fix point of some simultaneous equations. This is done via an iterative process. In this paper, we argue that data flow analysis can be understood as a temporal logic model checking problem. We illustrate this idea using the problem of live variable analysis. This provides a new perspective for understanding data flow analysis.

Introduction

Data flow analysis is a commonly used technique [1,2]. Many interesting problems in compilers and program analysis can be solved using this technique. For example, the problem of live variable analysis can be solved this way.

Here is a very simple example, illustrating the idea of live variable analysis. Consider the following C code.

```c
int main()
{
   int x;
   x = 2;
   x= 200;
   printf("%d", x);
   return 0;
}
```

The assignment statement on line 4 can be eliminated for the following reason. Variable x is about to be updated to 200 on line 5. So no matter what value x gets on line 4, it has no effect on the statement on line 6 at all. Hence the statement on line 4 can be safely removed. More formally, this is done using data flow analysis. We do this analysis backwards starting from line 8. We carefully analyze the set of live variables on each line. In particular, the set of live variables on line 4 is the empty set. This means that no variable is useful at all after executing the statement on line 4. So we can get rid of that statement on line 4.

In the next section, we will briefly review data flow analysis. And then we will summary the idea of model checking. After that we will present the idea of viewing data flow analysis as model checking. This is done using the example of live variable analysis. Finally we conclude in the last section.

Data Flow Analysis

Data flow analysis is a technique that can be used to obtain liveness information throughout the program [3,4]. Here is the basic idea. A variable is live means that the variable may potentially be used later on in the program. Each statement in the program may generate new live variables or remove existing live variables. We associate the following sets with each statement s in the
Definition 1. Given a statement $s$, we define the following sets.

- $\text{kill}(b) = \{v | v$ is a variable, and $v$ is defined in $s\}$
- $\text{generate}(s) = \{v | v$ is a variable, and $v$ is used in $s\}$
- $\text{in}(s) = \{v | v$ is a variable, and $v$ is live upon entering $s\}$
- $\text{out}(s) = \{v | v$ is a variable, and $v$ is live when exiting $s\}$
- $\text{succ}(s) = \{c | c$ is a statement, and $c$ is a successor of $s$ in control flow graph$\}$
- $\text{pred}(s) = \{c | c$ is a statement, and $c$ is a predecessor of $s$ in control flow graph$\}$

Suppose that $P$ is some program, we can compute $\text{generate}(s)$ and $\text{kill}(s)$, for each statement $s$ in program $P$. After that we can compute $\text{in}(s)$ and $\text{out}(s)$ according to the following set of rules.

$$\text{in}(s) = \text{out}(s) + \text{generate}(s) - \text{kill}(s) \quad \text{(Rule 1)}$$

$$\text{out}(s) = \bigcup_{c \in \text{succ}(s)} \text{in}(c) \quad \text{(Rule 2)}$$

### Model Checking

Given a Kripke structure $M = (S, R, L)$ that describes a finite state system and a temporal logic formula $f$ describing some specification. The model checking problem is to check if $M$ satisfies $f$ [5,6]. Usually this is done by finding the set of all states in $S$ that satisfy $f$. Here are the semantics of the temporal logic formulas.

Given an infinite sequence of states $s_0s_1s_2\ldots$, we write $S^k$ for the suffix of $S = s_0s_1s_2\ldots$ starting at $s_k$.

- $S^k \models f$, when $s_k \models f$
- $S^k \not\models f$, when not $s_k \models f$
- $S^k \models p \lor q$, when $s_k \models p$ or $s_k \models q$
- $S^k \models p \land q$, when $s_k \models p$ and $s_k \models q$
- $S^k \models p \bigcup q$, when there exists some $i > k$ s.t. $s_i \models q$ and for all $j$ s.t. $k < j < i$, $s_j \models p$
- $S^k \models p \bigcap q$, when either for all $i > k$, $s_i \models q$, or for some $j > k$, $s_j \models p$ and for all $i$ s.t. $k < i < j$, $s_i \models q$

Many efficient implementations of model checking tools are based on the fixpoint characterization of the temporal logic operators. And the process of solving this problem is an iterative process.

### Data Flow Analysis as Model Checking

In this section, we argue that we can view data flow analysis problem as a model checking problem. We illustrate this idea by using live variable analysis as an example.

Let us go back to the example from the introduction. Suppose that we want to get the liveness information for all program points. One way of doing this is to use the usual way of data flow analysis as is described in the previous section. Here is another way to think about the problem. If a variable is live at some point, that means that the variable must be used before it is redefined. In other words, it means that we have two events. a) The variable is used. b) The variable is redefined. If we use the idea of model checking, this can be rephrased as follows. A variable is live at some point in the program means that the variable is used “releases” the obligation that it cannot be redefined. So, using the “release” operator, this means “used R ~defined”. So the live variable analysis problem can be seen as a special case of model checking.

### Summary

In this paper, we propose to use another perspective to understand data flow analysis. The traditional approach for solving data flow analysis is to solve for the fixed point of some
simultaneous equations. This is done through an iterative process. We argue that the same problem can be solved using model checking techniques. We illustrate the idea using live variable analysis as an example. The mean that a certain variable is live can be captured by a temporal logic formula, thus the problem can be seen as a special form of model checking.

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References


