An Novel Power Flow Calculation Model with the Elimination of Interconnecting Nodes

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Abstract. Plenty of transmission substations in large scale power system are designed to deliver power to electricity users far away from power plants, and most of them can be seen as interconnecting (IC) nodes. Interconnecting node has the characteristic of zero power injection, thereafter causes zero current injection to the power system. A novel power flow calculation model is proposed, based on the elimination of IC node equations, to get more fast power flow calculation. The power network equations of IC nodes are eliminated so as to reduce the total dimension of the power flow model. Then, element-wise Jacobian matrix are deduced according to the reduced system power balance equations, in relationship to partial voltage vectors of non-interconnecting nodes. Tests on IEEE standard systems are then performed to validate the proposed method, and computation efficiency are compared with the traditional formulation of power flow model.

1. Introduction

Power flow calculation is one of the most fundamental calculation in power system analysis, operation and control, which is a nonlinear power equation solving problem [1]. Traditional power flow calculation aims at computing the nodal voltage amplitude, phase angle under polar coordinate, or in obtaining the real and imaginary parts of the voltage vector under rectangular coordinates. In order to get the power flow solution, Newton-Raphson method is always used for solving linearized power equations [2],[3], and it is able to solve most power flow cases, which has made it the most widely used algorithm in modern power systems.

Since the number of real and reactive power equations is close to twice of the number of buses, the computation time will increase dramatically in large scale power systems. It is well-known that there are plenty of transmission substations in large scale power system, delivering power to electricity users far away from power plants, and most of them can be seen as interconnecting (IC) nodes during power flow calculation. Interconnecting node has the characteristic of zero power injection, thereafter causes zero current injection to the power system. Some optimal power flow applications has introduced current balance equations as equality constraints [4],[5], which divided all buses into two types: the buses with non-zero injections and the buses with zero injections, and makes the total number constraints decrease significantly, which seems very effective for future steady state power flow calculations. Therefore, a new idea of calculating power flow based on the IC nodes reduction technique, seems to be prospective when the power system has a considerable large portion of IC nodes. In fact, large scale power systems with bulk, long distance transmission system substations are mostly the case.

In this paper, a novel power flow calculation model is proposed, based on the elimination of IC node equations, to get more fast power flow calculation. Firstly, the power network equations of IC nodes are eliminated so as to reduce the total dimension of the power flow model in section 2. Then, element-wise Jacobian matrix are deduced according to the reduced system power balance equations, in relationship to partial voltage vectors of non-interconnecting nodes, in section 3. Tests on IEEE standard systems are then performed to validate the proposed method in section 4, and computation efficiency are compared with traditional formulation of power flow model.
2. New model for power flow calculation

In an electric power network with \( n \) buses, nodal equation could be expressed according to the following matrix form:

\[
I = YV
\]

Where:

\[
\begin{bmatrix}
i_1 \\
i_2 \\
\vdots \\
i_n
\end{bmatrix}
= \begin{bmatrix}
\dot{v}_1 \\
\dot{v}_2 \\
\vdots \\
\dot{v}_n
\end{bmatrix}
= \begin{bmatrix}
y_{11} & y_{12} & \cdots & y_{1n} \\
y_{21} & y_{22} & \cdots & y_{2n} \\
\vdots & \vdots & \ddots & \vdots \\
y_{n1} & y_{n2} & \cdots & y_{nn}
\end{bmatrix}
\]

are the nodal injection current vector, and nodal voltage vector, and the node admittance matrix of the network, respectively.

### 2.1 Interconnecting node elimination

Due to the reason that the injection currents for IC nodes are all zero, this part of the network equation can be set apart. Suppose there are \( m \) IC nodes in the power system, and non-interconnecting nodes will be \( n = (n_i - m_i) \), the network equations can be represented as:

\[
\begin{bmatrix}
0 \\
I_n
\end{bmatrix} = \begin{bmatrix}
y_{11} & y_{12} & \cdots & y_{1n} \\
y_{21} & y_{22} & \cdots & y_{2n} \\
\vdots & \vdots & \ddots & \vdots \\
y_{n1} & y_{n2} & \cdots & y_{nn}
\end{bmatrix} \begin{bmatrix}
V_1 \\
V_2 \\
\vdots \\
V_n
\end{bmatrix}
\]

The admittance matrix block \( Y_{11} \) is typically non-singular, as the full admittance should be non-singular so as to get a power flow solution. Then the node voltage block \( V_1 \) can be eliminated:

\[
V_1 = -(Y_{11})^{-1}Y_{12}V_2
\]

After the elimination of IC nodes, a reduced admittance matrix is then obtained.

\[
Y_\text{reduced} = Y_{22} - Y_{21}(Y_{11})^{-1}Y_{12}
\]

The network equation of Eqn. (3) can be acquired as:

\[
I_2 = Y_\text{reduced}V_2
\]

### 2.2 Dimension reduction of the power flow model

Noticing the definition of power system complex power:

\[
S = V(I)^*
\]

Then the power balance equations of Eqn. (3) can be reduced to a dimension of \( n = (n_i - m_i) \) as:

\[
S_2 = V_2(I_2)^* = V_2(Y_\text{reduced}V_2)^*
\]

Polar coordinate form of node power equations are written as [1]:

\[
\begin{cases}
P_{\text{r}} - P_{\text{i}} = U_j \sum_{j=1}^{n} U_j \left( G_{ij} \sin \theta_j + B_{ij} \cos \theta_j \right) = 0 \\
Q_{\text{r}} - Q_{\text{i}} = U_j \sum_{j=1}^{n} U_j \left( G_{ij} \sin \theta_j - B_{ij} \cos \theta_j \right) = 0
\end{cases}
\]

### 2.3 Matrix form of the dimensional reduced power flow model

The network injection power of Eqn. (9) can be transformed to a matrix form, which is more suitable to be coded in Matlab software:

\[
\begin{aligned}
&= U_j \sum_{j=1}^{n} U_j \left( G_{ij} \cos \theta_j + B_{ij} \sin \theta_j \right) \\
&= U_j \sum_{j=1}^{n} U_j \left( G_{ij} \cos \theta_j + B_{ij} \sin \theta_j \right) + \cdots + U_j \sum_{j=1}^{n} U_j \left( G_{ij} \cos \theta_j + B_{ij} \sin \theta_j \right) \\
&= \text{diag}[U] \cdot \text{diag}[G \cos \theta + B \sin \theta] \cdot \text{diag}[U].
\end{aligned}
\]

where: the symbol \( \text{diag}[\cdot] \) means diagonalization of a column vector.
The complete power flow model can be obtained as:
\[
\begin{bmatrix}
P - \text{diag}(U)[G \cos \theta + B \sin \theta][U] = 0 \\
Q - \text{diag}(U)[G \cos \theta - B \sin \theta][U] = 0
\end{bmatrix}
\]

Using the prosthaphaeresis formula:
\[
\begin{align*}
\cos x \cos y + \sin x \sin y &= \cos(x - y) \\
\sin x \cos y - \cos x \sin y &= \sin(x - y)
\end{align*}
\]

Then the power flow model yields:
\[
\begin{bmatrix}
P_y - \text{diag}(U)[|Y| \cdot \cos(\delta_y)][U] = 0 \\
Q_y - \text{diag}(U)[|Y| \cdot \sin(\delta_y)][U] = 0
\end{bmatrix}
\]

In which the element-wise variables can be deduced as:
\[
\begin{bmatrix}
Y_{ij} \\
\vdots \\
Y_{in}
\end{bmatrix}
\begin{bmatrix}
\delta_{ij} \\
\vdots \\
\delta_{in}
\end{bmatrix}
= \begin{bmatrix}
V_i \\
\vdots \\
V_n
\end{bmatrix}
\begin{bmatrix}
\cos \theta \\
\vdots \\
\cos \theta
\end{bmatrix}
\]

Where: \(|Y|\) and \(\delta_y\) are the modulus and phase angle values of the dimension-reduced admittance matrix (5).

Finally, the linearized power flow equations are expressed as:
\[
\begin{bmatrix}
\Delta P^{(i)} \\
\Delta Q^{(i)}
\end{bmatrix}
= -\begin{bmatrix}
H^{(i)} & J^{(i)} \\
N^{(i)} & L^{(i)}
\end{bmatrix}
\begin{bmatrix}
\Delta \theta^{(i)}
\end{bmatrix}
\]

Similarly, the matrices for all Jacobian elements can be derived, the boldface terms all denote the matrices form.

\[
\begin{bmatrix}
H_y = \frac{\partial \Delta P}{\partial \theta_j} = -U_j Y_y \sin(\delta_j) \\
H_a = \frac{\partial \Delta P}{\partial \theta_j} = U_j \sum_{j'=1}^n U_j Y_{y'j'} \sin(\delta_{j'}) - U_i Y_i \sin(\delta_y) \\
H = \text{diag}(U) * \text{diag}[|Y| * \sin(\delta_y) * (U)] - \text{diag}(U) * [|Y| * \sin(\delta_y)] * \text{diag}(U)
\end{bmatrix}
\]

\[
\begin{bmatrix}
N_y = \frac{\partial \Delta P}{\partial U_j} = -U_i Y_j \cos(\delta_j) \\
N_a = \frac{\partial \Delta P}{\partial U_i} = -U_i \sum_{j'=1}^n U_j Y_{y'j'} \cos(\delta_{j'}) - U_i Y_i \cos(\delta_y) \\
N = -\text{diag}(U) * \text{diag}[|Y| * \cos(\delta_y) * (U)] - \text{diag}(U) * [|Y| * \cos(\delta_y)] * \text{diag}(U)
\end{bmatrix}
\]

\[
\begin{bmatrix}
J_y = \frac{\partial \Delta Q}{\partial \theta_j} = U_i Y_y \cos(\delta_y) \\
J_a = \frac{\partial \Delta Q}{\partial \theta_j} = -U_i \sum_{j'=1}^n U_j Y_{y'j'} \cos(\delta_{j'}) - U_i Y_i \cos(\delta_y) \\
J = -\text{diag}(U) * \text{diag}[|Y| * \cos(\delta_y) * (U)] + \text{diag}(U) * [|Y| * \cos(\delta_y)] * \text{diag}(U)
\end{bmatrix}
\]
\[
L_y = \frac{\partial \Delta Q_i}{\partial U_j} = U_i U_j \sin(\delta_y) \\
L_x = \frac{\partial \Delta Q_i}{\partial U_i} = -U_i \sum_{j=i} Y_j \sin(\delta_y) - U_i U_s \sin(\delta_y) \\
L = -\text{diag}(Y)*\text{diag}(|Y|.*\sin(\delta_y).*(U)) + \text{diag}(U)*[|Y|.*\sin(\delta_y)]*\text{diag}(U)
\]

(19)

It can be seen from the power mismatch formula (13) and Jacobian matrices of (16) to (19) that, the element of the Jacobian matrix has similar features to traditional power-equation-voltage-variable type power flow calculation:

1) The dimension of the Jacobian matrix is $2(n-1)$, much less than that of the traditional type of $2(n+m-1)$, because active and reactive power of the IC nodes are zero, thereafter the $2m$ power equations are eliminated from the new power flow model naturally;

2) If the mutual admittance between node $i$ and $j$ is zero, the corresponding element in the Jacobian matrix is zero, namely, the new type of Jacobian matrix is still sparse;

3) From two off-diagonal blocks of the Jacobian matrix are also symmetric, which could save computation time potentially as traditional power flow model;

4) The difference between the proposed new model and traditional power flow model is that, the admittance matrix has a reduced dimension. In the meantime, the reduced admittance matrix $Y_{\text{reduced}}$ in Eqn. (5) includes an inverse calculation of $m_i$ dimension of $Y$. The power flow computational efficiency will highly depend on both the ratio of IC nodes in the power system and the total number of nodes of the power system. Typical standard systems will be tested in next section to validate the effectiveness of the proposed model.

3. Case studies

The proposed new power flow model with the elimination of IC nodes are coded in Matlab software, and typical IEEE standard systems [6], and several large scale real world systems are tested on the proposed model.

3.1 Initial settings for the proposed power flow model

Typically in the traditional power flow model, the initial voltage value can be selected by empirical settings, such as flat starting with voltage magnitude as 1 p.u. and phase angle as zero degree, would be quite robust. As for the proposed new power flow model, it can be the same, then the Newton-Raphson iteration can be performed.

As for the PV type nodes, their voltage magnitude can be set as their pre-given values.

3.2 Convergence performance of the new power flow model

The maximum power mismatches in relationship with iteration numbers are plotted in Fig. 1. The convergence threshold is set as 10-8, and it can be seen that all test systems can converge in 4 to 6 steps, which proves the effectiveness of the proposed dimension reduced power flow model.
3.3 Comparison to traditional power flow model

Further result in Table I shows the voltage magnitudes and phase angles of IEEE-14 system. It can be seen that the power flow results by the proposed model are the same to the traditional power flow model, according to the same initial condition and convergence criteria.

Table I: Convergence result of IEEE-14 bus systems

<table>
<thead>
<tr>
<th>Node No. of IEEE-14</th>
<th>Traditional model</th>
<th>New power flow model</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Voltage Magnitude (p.u.)</td>
<td>Phase Angle (°)</td>
</tr>
<tr>
<td>1</td>
<td>1.06000000</td>
<td>0.00000000</td>
</tr>
<tr>
<td>2</td>
<td>1.04500000</td>
<td>-4.98258914</td>
</tr>
<tr>
<td>3</td>
<td>1.01000000</td>
<td>-12.72509994</td>
</tr>
<tr>
<td>4</td>
<td>1.01767085</td>
<td>-10.31290109</td>
</tr>
<tr>
<td>5</td>
<td>1.01951386</td>
<td>-8.77385390</td>
</tr>
<tr>
<td>6</td>
<td>1.07000000</td>
<td>-14.22094646</td>
</tr>
<tr>
<td>7</td>
<td>1.06151953</td>
<td>-13.35962737</td>
</tr>
<tr>
<td>8</td>
<td>1.09000000</td>
<td>-13.35962737</td>
</tr>
<tr>
<td>9</td>
<td>1.05593172</td>
<td>-14.93852130</td>
</tr>
<tr>
<td>10</td>
<td>1.05098462</td>
<td>-15.09728846</td>
</tr>
<tr>
<td>11</td>
<td>1.05690652</td>
<td>-14.79062203</td>
</tr>
<tr>
<td>12</td>
<td>1.05518856</td>
<td>-15.07558452</td>
</tr>
<tr>
<td>13</td>
<td>1.05038171</td>
<td>-15.15627634</td>
</tr>
<tr>
<td>14</td>
<td>1.03552995</td>
<td>-16.03364453</td>
</tr>
</tbody>
</table>

In addition, Table II gives a quantitative analysis on the computational superiority of the proposed model, especially for large scale power systems with node number more than 300. The calculation time for 703-bus and 1047-bus can be reduced by over 50%, because the IC node ratio is actually high in large scale power systems. The computational effect is not time-saving enough for power systems with node number less than 118, mainly because the computation burden of inverse calculation of of $Y_{11}$ during the admittance matrix dimension reduction.
Table II: Computation efficiency comparison between the new model and traditional power flow model

<table>
<thead>
<tr>
<th>Node No. of test systems</th>
<th>No. of IC nodes</th>
<th>Node reduction Ratio</th>
<th>CPU Time for traditional PF model</th>
<th>CPU Time of New model</th>
</tr>
</thead>
<tbody>
<tr>
<td>IEEE 14</td>
<td>1</td>
<td>7.14%</td>
<td>0.020804</td>
<td>0.039510</td>
</tr>
<tr>
<td>IEEE 30</td>
<td>6</td>
<td>20.00%</td>
<td>0.021347</td>
<td>0.042440</td>
</tr>
<tr>
<td>IEEE 57</td>
<td>15</td>
<td>26.32%</td>
<td>0.025704</td>
<td>0.048579</td>
</tr>
<tr>
<td>IEEE 118</td>
<td>10</td>
<td>8.47%</td>
<td>0.042031</td>
<td>0.063369</td>
</tr>
<tr>
<td>IEEE 300</td>
<td>66</td>
<td>22.00%</td>
<td>0.183164</td>
<td>0.157452</td>
</tr>
<tr>
<td>C703</td>
<td>381</td>
<td>54.20%</td>
<td>1.491008</td>
<td>0.519730</td>
</tr>
<tr>
<td>N1047</td>
<td>540</td>
<td>51.58%</td>
<td>3.289168</td>
<td>1.407905</td>
</tr>
</tbody>
</table>

4. Conclusions

In modern large scale power systems, a large number of transmission substations are designed to deliver power to electricity users far away from power plants. And most of them are interconnecting (IC) nodes, which has the characteristic of zero power injection, thereafter causes zero current injection to the power system. A novel power flow calculation model is proposed, based on the elimination of IC node equations, to get more fast power flow calculation. The power network equations of IC nodes are eliminated so as to reduce the total dimension of the power flow model. Based on a deduction of power flow Jacobian in matrices forms, computational tests on IEEE standard systems and several real world large power systems are then performed to validate the proposed method, it is proved that the proposed method has excellent convergence performance as the traditional formulation of power flow model. Comparing to the traditional power flow model, the computation efficiency of our model is able to reduce the CPU time by over 50% for large scale power systems with node number more than 300 buses.

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