Power Flow Algorithm for Hybrid AC-DC System Equipped With VSC-HVDCs

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Abstract—The emergence of full-controlled power electronic devices, such as VSC, provides a new direction for the development of HVDC. Based on the analysis of the steady-state model of VSC-HVDC, its mathematical model for Newton method power flow calculation is developed. This paper proposes a uniform iterative power flow calculation algorithm for systems equipped with VSC-HVDCs. Simulative results for the modified IEEE-14 node test system demonstrate the validity and accuracy of the algorithm.

Keywords- VSC-HVDC; Voltage source converter; power electronic; MTDC; Power flow calculation

I. INTRODUCTION

In 1954, the first high-voltage direct current (HVDC) power transmission project was put into industrialized operation. Since the 1990s, with the rapid development of power electronic devices and control technology, voltage source inverter VSC that is on the basis of the whole control type which can be shut off the device type of is more mature. On the basis of this, new type of high voltage direct current transmission (VSC-HVDC) technology has developed. Compared with the traditional transmission which bases on thyristor dc, it has many advantages which can provide to the isolated load power supply directly and is more economy to the load center over a long distance transmission, etc. [1–3]. The MTDC which is on the basis of 2 end HVDC can achieve more power and placement by electricity, which has more and more attention.

Currently, as for ac/dc hybrid transmission system of method research of power flow calculation, there are mainly alternative method and unified iterative method. Alternative method can achieve the dc current and ac current iteration separately as well as can inherit the original pure ac power flow calculation program through solving the dc system and ac system respectively. But alternative iteration process did not consider the coupling relationship between dc network and communication network, which is not high accuracy, poor convergence, especially on the multi-terminal dc system and the weak ac system, due to the strong coupling between ac/dc, when people use the alternative method, the deterioration of convergence will be appeared and power flow even are not convergence. [4] On the basis of analysis of VSC - HVDC steady state model, an ac/dc hybrid system power flow alternate algorithm is put forward in references[5]. According to the amount of adjustment system of inverter control, the different combination of M and phase Angle of the delta, references[6] list four kinds of operational control scheme and for each control scheme that is given, its ac/dc trend of alternative interface equation. Unified iterative method is the trend of dc system equations and equations with tidal current communication system, which can have a unified solution with an unknown quantity in the ac/dc system. This method can calculate the power flow accurately and have a good convergence. According to different control mode VSC, references[7] deduce the corresponding tide correction equation and proposed unified iterative algorithm that is VSC - HVDC ac/dc hybrid system trend and references[8] study the two end of the renewable energy ac/dc power system and power flow calculation. These documents do not use the unified iterative method to analyze multiterminal tide problem of ac/dc hybrid system and the convergence of power flow calculation should be improved.

In this paper, that is based on the principle of VSC - HVDC and steady-state model, communication network and dc network correction equation were deduced respectively. Based on Newton method, it proposed for MTDC unified iterative method of ac/dc power flow calculation method, which achieved with multiterminal dc power flow calculation of power system.

II. VSC - HVDC STEADY STATE MODEL

Voltage source inverter connected to the ac bus by converter transformer. For multiterminal of VSC transmission system, the voltage source inverter is connected with the dc circuit. For the ith a VSC, the steady-state model is shown in Fig.1.
In picture 1, the $i$ represents the $i$th inverter dc network. Converter internal loss and loss of converter, transformer for $R$ and the converter transformer impedance for $X$. The current through the converter transformer is $Q_c$. Communication system into the converter transformers active power and reactive power respectively. $P_c$ and $Q_c$. VSC has absorption of active power and reactive power of $P_c$ and $Q_c$ respectively. Dc voltage for $U_{dc}$, ac current for $I_a$, ac busbar voltage fundamental component for $U_a$, phase Angle for the delta $s$, inverter ac bus voltage fundamental component for $U_n$, phase Angle for the delta $c$.

When inverter resistance and harmonic component are ignored, it can get VSC and communication system between the transmission of active power and reactive power respectively.

$$p_i = \frac{U_i U_a}{X} \sin \delta_i$$  (1)

$$q_i = \frac{U_i}{X} (U_a - U_i \cos \delta_i)$$  (2)

$$\delta_i = \delta_{ii} - \delta_{ii}$$  (3)

By type (1), (2), active power transmission depends mainly on the delta and reactive power transmission mainly depends on $U_a$. By controlling the phase Angle of the delta and $U_a$, it can be realized to size and direction of active power as well as reactive power control. While $U_a$ by dc voltage $U_d$ is associated with mode modulation M (0 $\leq M \leq 1$) of pulse width modulation (PWM) and Dc voltage utilization and mu (0 $< \mu \leq 1$), which are decided both.

$$U_i = \frac{2M}{\sqrt{2}} U_d$$  (4)

III. VSC - HVDC CONVERTER OPERATION CONTROL MODE

VSC - HVDC uses control switch device, so each inverter has two control volume, PWM modulation M and phase Angle of the delta. Inverter can be independent regulation of active power and reactive power. To ensure the normal and stable operation of VSC – HVDC, it must make dc network active power balance. The input dc network active power must be equal with the dc network output active power and dc network active power loss. Otherwise, it will cause the system dc voltage fluctuations. Usually, in the multi-terminal dc system, at it must select no more than one inverter control of the dc side voltage, as the balance of active power inverter dc network. So the operation of the VSC - HVDC voltage source inverter control method has four kinds:

a. $U_d$ constant dc voltage, ac reactive power $Q_c$

b. $U_d$ constant dc voltage, ac busbar voltage $U_n$

c. Constant communication active power $P_s$, ac reactive power $Q_s$

d. Ac active power $P_n$, ac busbar voltage $U_n$

IV. VSC - HVDC AC-DC POWER FLOW CALCULATION

The ac/dc transmission system is divided into communication subsystem and dc subsystem analyzed. As for communication subsystem, it can be divided into two classes through the node connected to the inverter: one kind can not directly connect with inverter of ordinary nodes with using subscript $a$; and the other kind is special nodes and inverter direct connection. With the subscript $t$. the ordinary nodes in the communication system, the node power deviation equation is identical to that of pure ac system.

$$\Delta P_a = P_{in} - U_a \sum_{i} (G_{ai} \cos \theta_i + B_{ai} \sin \theta_i)$$  (5)

$$\Delta Q_a = Q_{in} - U_a \sum_{i} (G_{ai} \sin \theta_i - B_{ai} \cos \theta_i)$$  (6)

As for special node, its power deviation equations include communication system into the ith a inverter power, so

$$\Delta P_a = P_{in} - U_a \sum_{i} (G_{ai} \cos \theta_i + B_{ai} \sin \theta_i) - P_a$$  (7)

$$\Delta Q_a = Q_{in} - U_a \sum_{i} (G_{ai} \sin \theta_i - B_{ai} \cos \theta_i) - Q_a$$  (8)

At this point, the VSC transmission between its ac busbar of active power and reactive power can be written

$$P_a = \frac{U_a}{\sqrt{2}} M U_d U_a Y_i \sin(\delta_i - \alpha) + U_a^{2} Y_i \sin \alpha_i$$  (9)

$$Q_a = \frac{U_a}{\sqrt{2}} M U_d U_a Y_i \cos(\delta_i - \alpha) + U_a^{2} Y_i \cos \alpha_i$$  (10)

$$Y_i = 1/\sqrt{R^2 + X^2} \cdot \alpha_i = \arctan(R / X)$$  (11)

Based on the established steady-state model of VSC - HVDC and dc network equation, it can deduce the power flow calculation of dc system equations.

$$\Delta d_{a} = P_{a} - \frac{U_a}{\sqrt{2}} M U_d U_a Y_i \cos(\delta_i + \alpha) - U_a^{2} Y_i \cos \alpha_i$$

$$\Delta d_{b} = Q_{a} + \frac{U_a}{\sqrt{2}} M U_d U_a Y_i \sin(\delta_i + \alpha) - U_a^{2} \sin \alpha_i$$

$$\Delta d_{c} = U_a I_a - \frac{U_a}{\sqrt{2}} M U_d U_a Y_i \cos(\delta_i - \alpha) + \frac{1}{2} (\mu M U_a) Y_i \cos \alpha_i$$

$$\Delta d_{d} = I_a - U_a \sum_{i} g_{ai} \Delta U_i$$

In this type: $g_{ai}$ is the dc network after elimination contact node admittance matrix elements.

Type5 to 8 with the type 11, according to the Taylor series expansion, after slightly higher order term, the Newton's method for power flow correction equation is

$$\Delta f = -J \Delta x$$  (12)

As written in matrix form:
In the type: N,H,L,J as the Jacobi matrix of pure ac system, $\Delta D = (\Delta d_1, \Delta d_2, \Delta d_3, \Delta d_4)^T$, $\Delta x = (\Delta U d, \Delta I d, \Delta \delta, \Delta \theta, \Delta M, \Delta \xi)$.

There is no direct connection with dc network due to the common node, so $\Delta P_d, \Delta Q_d$ for $\Delta x_d$ partial derivatives of 0. Similarly, $\Delta D_d$ on all nodes of the phase Angle $\theta_i$ beg partial derivatives are zero.

For systems with $n$ nodes, if you have $n_t$ a voltage source inverter, the type (12) is with a total of $2(n-1)+4n_t$ variables. According to the given inverter control mode, it can eliminate $4n_t$ variables, so the type (12) can be solved. Compared with traditional power flow calculation, the system with VSC, Jacobian matrix augmented for order.

Unified iterative method VSC - HVDC ac/dc power flow calculation flow chart is shown in Fig.2.

For the modified IEEE - 14 node example in this paper and a flow algorithm with alternating iterative method is presented. Under the condition of the same convergence precision, the number of iterations is 8 times and 4 times respectively, which proves that this algorithm has good convergence.

**TABLE I. CALCULATION RESULTS COMMUNICATION TENDENCY**

<table>
<thead>
<tr>
<th>node</th>
<th>voltage amplitude</th>
<th>phase angle / (°)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1.060 0</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>1.045 0</td>
<td>-4.982 0</td>
</tr>
<tr>
<td>3</td>
<td>1.010 0</td>
<td>-12.719 2</td>
</tr>
<tr>
<td>4</td>
<td>1.018 2</td>
<td>-10.314 2</td>
</tr>
<tr>
<td>5</td>
<td>1.020 0</td>
<td>-8.780 3</td>
</tr>
<tr>
<td>6</td>
<td>1.070 0</td>
<td>-14.253 9</td>
</tr>
<tr>
<td>7</td>
<td>1.060 5</td>
<td>-13.342 4</td>
</tr>
<tr>
<td>8</td>
<td>1.090 0</td>
<td>-13.342 4</td>
</tr>
<tr>
<td>9</td>
<td>1.053 5</td>
<td>-14.916 1</td>
</tr>
<tr>
<td>10</td>
<td>1.049 0</td>
<td>-15.084 2</td>
</tr>
<tr>
<td>11</td>
<td>1.055 9</td>
<td>-14.798 8</td>
</tr>
<tr>
<td>12</td>
<td>1.055 4</td>
<td>-15.108 6</td>
</tr>
<tr>
<td>13</td>
<td>1.050 5</td>
<td>-15.213 5</td>
</tr>
<tr>
<td>14</td>
<td>1.032 7</td>
<td>-15.958 5</td>
</tr>
</tbody>
</table>

V. THE EXAMPLE ANALYSIS

In this paper, we use the modified IEEE - 14 nodes system as an example to verify the effectiveness of the algorithm. In the Fig. 2:

VSC1, VSC2 and VSC3 are on busbar 12, busbar 13 and busbar 14 respectively; $R = 0.006, X = 0.15$. VSC1 adopts constant dc voltage ($U_{d1ref} = 2.0$) Ac reactive power ($Q_{s1ref} = 0.007$). VSC2 adopts fixed exchange active power ($P_{s2ref} = 0.0429$), ac reactive power control ($Q_{s2ref} = -0.0552$), ac reactive power ($Q_{s3ref} = -0.0137$). Dc resistance network $R_d = 0.03$, dc voltage utilization $\mu = 1$.

Power flow calculation results are shown in table 1 and table 2. From the table 2, in this article, the algorithm can converge to the VSC control target. Besides, VSC1, and VSC2 absorbs the active power from the ac power grid and the VSC3 injected active power to the grid for achieving the active power balance in the system.

Program convergences after 4 iterations. References[9] takes the same IEEE - 14 node example in this paper and a flow algorithm with alternating iterative method is presented. Under the condition of the same convergence precision, the number of iterations is 8 times and 4 times respectively, which proves that this algorithm has good convergence.

**TABLE II. DC CURRENT CALCULATION RESULTS**

<table>
<thead>
<tr>
<th>inverter</th>
<th>$U_d$</th>
<th>$I_d$</th>
<th>$\delta$ (°)</th>
<th>$M$</th>
<th>$P_s$</th>
<th>$Q_s$</th>
</tr>
</thead>
<tbody>
<tr>
<td>VSC1</td>
<td>2.000</td>
<td>0.0079</td>
<td>0.1207</td>
<td>0.8608</td>
<td>0.0139</td>
<td>0.0070</td>
</tr>
<tr>
<td>VSC2</td>
<td>1.9998</td>
<td>0.0214</td>
<td>0.3321</td>
<td>0.8567</td>
<td>0.0429</td>
<td>0.0078</td>
</tr>
<tr>
<td>VSC3</td>
<td>1.9989</td>
<td>-0.0294</td>
<td>-0.4678</td>
<td>0.8456</td>
<td>-0.0552</td>
<td>-0.0137</td>
</tr>
</tbody>
</table>
VI. CONCLUSION

Ac/dc hybrid transmission system with the VSC flow calculation is the basis of studying the characteristics of VSC - HVDC steady state. It also analyzes the steady state control mode and the corresponding control necessary for protection technology. Due to multiterminal HVDC system’s many advantages, the analysis has a realistic significance. This paper presents a ac/dc system power flow calculation and unified iterative method to modify the IEEE system - 14 nodes to calculate, which verifies the accuracy of trend correctness and validity of iterative algorithm in this paper.

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REFERENCES


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